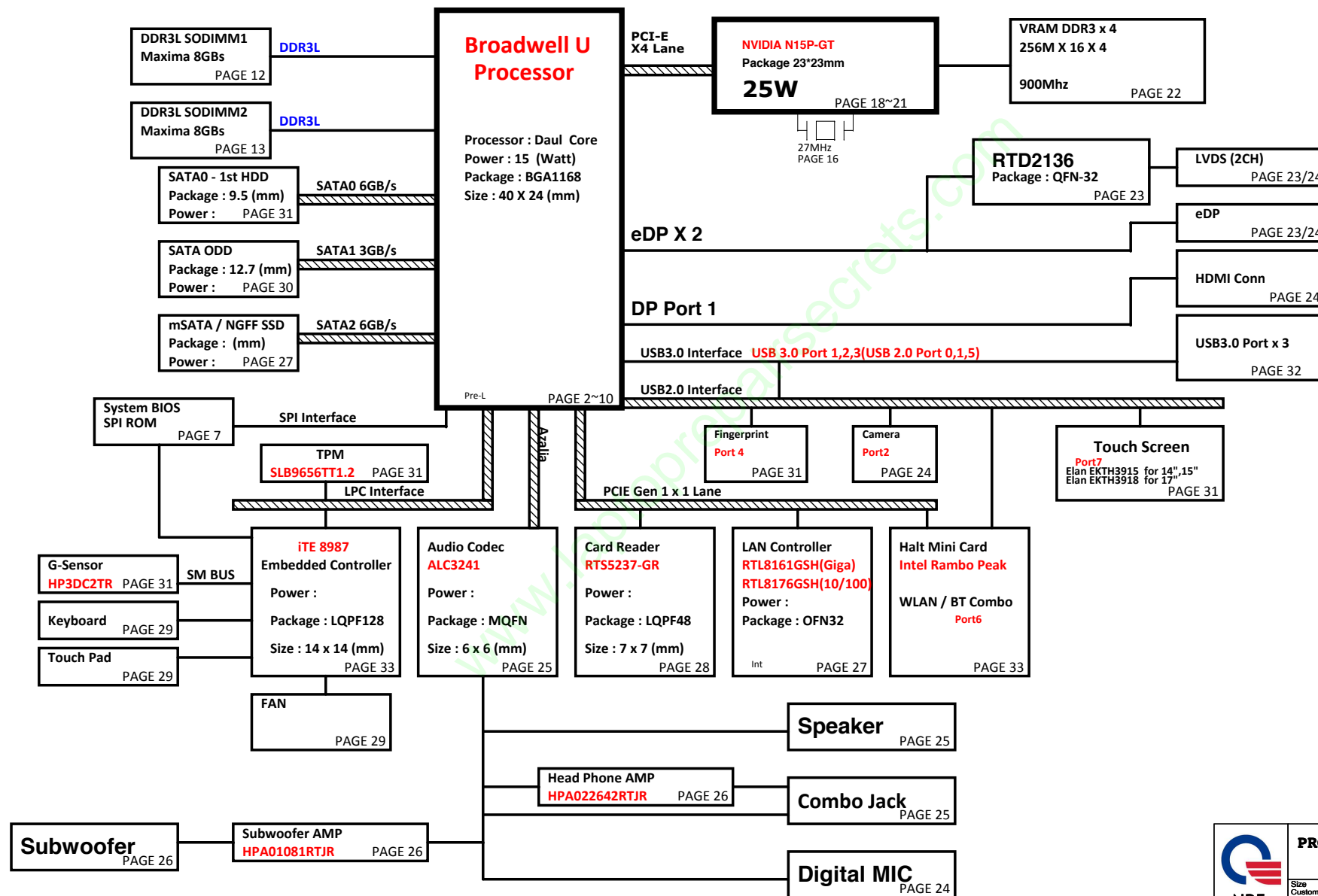


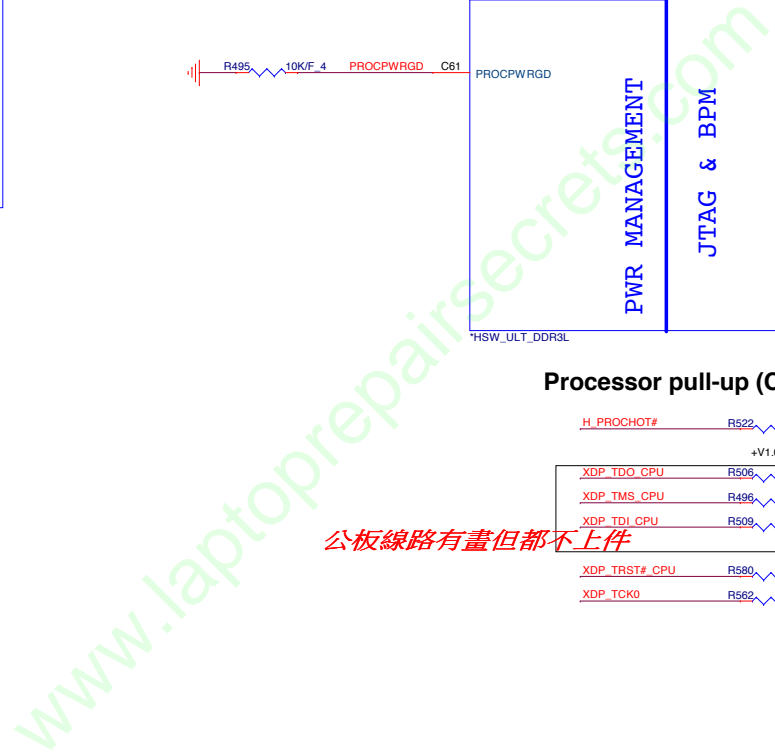
# DIS (14" / 15" / 17") S-Vine<sup>MT</sup> Intel Crescent Bay ULT Platform Block Diagram

01

PCB 6L STACK UP

LAYER 1 : TOP  
LAYER 2 : SGND  
LAYER 3 : IN1(High)  
LAYER 4 : IN2(Low)  
LAYER 5 : SVCC  
LAYER 6 : BOT





H\_PROCHOT# R522 62.4 +V1.05S\_VCCST  
 +V1.05S\_VCCST  
 XDP\_TDO\_CPU R506 51.4  
 XDP\_TMS\_CPU R496 51.4  
 XDP\_TDI\_CPU R509 51.4  
 XDP\_TRST#\_CPU R580 51.4  
 XDP\_TCK0 R562 51.4

公板線路有畫但都不上件

eDP\_COMPIO and ICOMPO signals should be shorted near balls and routed with typical impedance <25 mohms

U25C

U25D

U25E

U25F

U25G

U25H

U25I

U25J

U25K

U25L

U25M

U25N

U25O

U25P

U25Q

U25R

U25S

U25T

U25U

U25V

U25W

U25X

U25Y

U25Z

U25AA

U25AB

U25AC

U25AD

U25AE

U25AF

U25AG

U25AH

U25AI

U25AJ

U25AK

U25AL

U25AM

U25AN

U25AO

U25AP

U25AQ

U25AR

U25AS

U25AT

U25AU

U25AV

U25AW

U25AX

U25AY

U25AZ

U25BA

U25BB

U25BC

U25BD

U25BE

U25BF

U25BG

U25BH

U25BI

U25BJ

U25BK

U25BL

U25BM

U25BN

U25BO

U25BP

U25BQ

U25BR

U25BS

U25BT

U25BU

U25BV

U25BW

U25BX

U25BY

U25BZ

U25CA

U25CB

U25CC

U25CD

U25CE

U25CF

U25CG

U25CH

U25CI

U25CJ

U25CK

U25CL

U25CM

U25CN

U25CO

U25CP

U25CQ

U25CR

U25CS

U25CT

U25CU

U25CV

U25CW

U25CX

U25CY

U25CZ

U25DA

U25DB

U25DC

U25DD

U25DE

U25DF

U25DG

U25DH

U25DI

U25DJ

U25DK

U25DL

U25DM

U25DN

U25DO

U25DP

U25DQ

U25DR

U25DS

U25DT

U25DU

U25DV

U25DW

U25DX

U25DY

U25DZ

U25EA

U25EB

U25EC

U25ED

U25EE

U25EF

U25EG

U25EH

U25EI

U25EJ

U25EK

U25EL

U25EM

U25EN

U25EO

U25EP

U25EQ

U25ER

U25ES

U25ET

U25EU

U25EV

U25EW

U25EX

U25EY

U25EZ

U25FA

U25FB

U25FC

U25FD

U25FE

U25FF

U25FG

U25FH

U25FI

U25FJ

U25FK

U25FL

U25FM

U25FN

U25FO

U25FP

U25FQ

U25FR

U25FS

U25FT

U25FU

U25FV

U25FW

U25FX

U25FY

U25FZ

U25GA

U25GB

U25GC

U25GD

U25GE

U25GF

U25GG

U25GH

U25GI

U25GJ

U25GK

U25GL

U25GM

U25GN

U25GO

U25GP

U25GQ

U25GR

U25GS

U25GT

U25GU

U25GV

U25GW

U25GX

U25GY

U25GZ

U25HA

U25HB

U25HC

U25HD

U25HE

U25HF

U25HG

U25HH

U25HI

U25HJ

U25HK

U25HL

U25HM

U25HN

U25HO

U25HP

U25HQ

U25HR

U25HS

U25HT

U25HU

U25HV

U25HW

U25HX

U25HY

U25HZ

U25IA

U25IB

U25IC

U25ID

U25IE

U25IF

U25IG

U25IH

U25II

U25IJ

U25IK

U25IL

U25IM

U25IN

U25IO

U25IP

U25IQ

U25IR

U25IS

U25IT

U25IU

U25IV

U25IW

U25IX

U25IY

U25IZ

U25JA

U25JB

U25JC

U25JD

U25JE

U25JF

U25JG

U25JH

U25JI

U25JJ

U25JK

U25JL

U25JM

U25JN

U25JO

U25JP

U25JQ

U25JR

U25JS

U25JT

U25JU

U25JV

U25JW

U25JX

U25JY

U25JZ

U25KA

U25KB

U25KC

U25KD

U25KE

U25KF

U25KG

U25KH

U25KI

U25KJ

U25KK

U25KL

U25KM

U25KN

U25KO

U25KP

U25KQ

U25KR

U25KS

U25KT

U25KU

U25KV

U25KW

U25KX

U25KY

U25KZ

U25LA

U25LB

U25LC

U25LD

U25LE

U25LF

U25LG

U25LH

U25LI

U25LJ

U25LK

U25LL

U25LM

U25LN

U25LO

U25LP

U25LQ

U25LR

U25LS

U25LT

U25LU

U25LV

U25LW

U25LX

U25LY

U25LZ

U25MA

U25MB

U25MC

U25MD

U25ME

U25MF

U25MG

U25MH

U25MI

U25MJ

U25MK

U25ML

U25MM

U25MN

U25MO

U25MP

U25MQ

U25MR

U25MS

U25MT

U25MU

U25MV

U25MW

U25MX

U25MY

U25MZ

U25NA

U25NB

U25NC

U25ND

U25NE

U25NF

U25NG

U25NH

U25NI

U25NJ

U25NK

U25NL

U25NM

U25NN

U25NO

U25NP

U25NQ

U25NR

U25NS

U25NT

U25NU

U25NV

U25NW

U25NX

U25NY

U25NZ

U25OA

U25OB

U25OC

U25OD

U25OE

U25OF

U25OG

U25OH

U25OI

U25OJ

U25OK

U25OL

U25OM

U25ON

U25OO

U25OP

U25OQ

U25OR

U25OS

U25OT

U25OU

U25OV

U25OW

U25OX

U25OY

U25OZ

U25PA

U25PB

U25PC

U25PD

U25PE

U25PF

U25PG

U25PH

U25PI

U25PJ

U25PK

U25PL

U25PM

U25PN

U25PO

U25PP

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U25PW

U25PX

U25PY

U25PZ

U25QA

U25QB

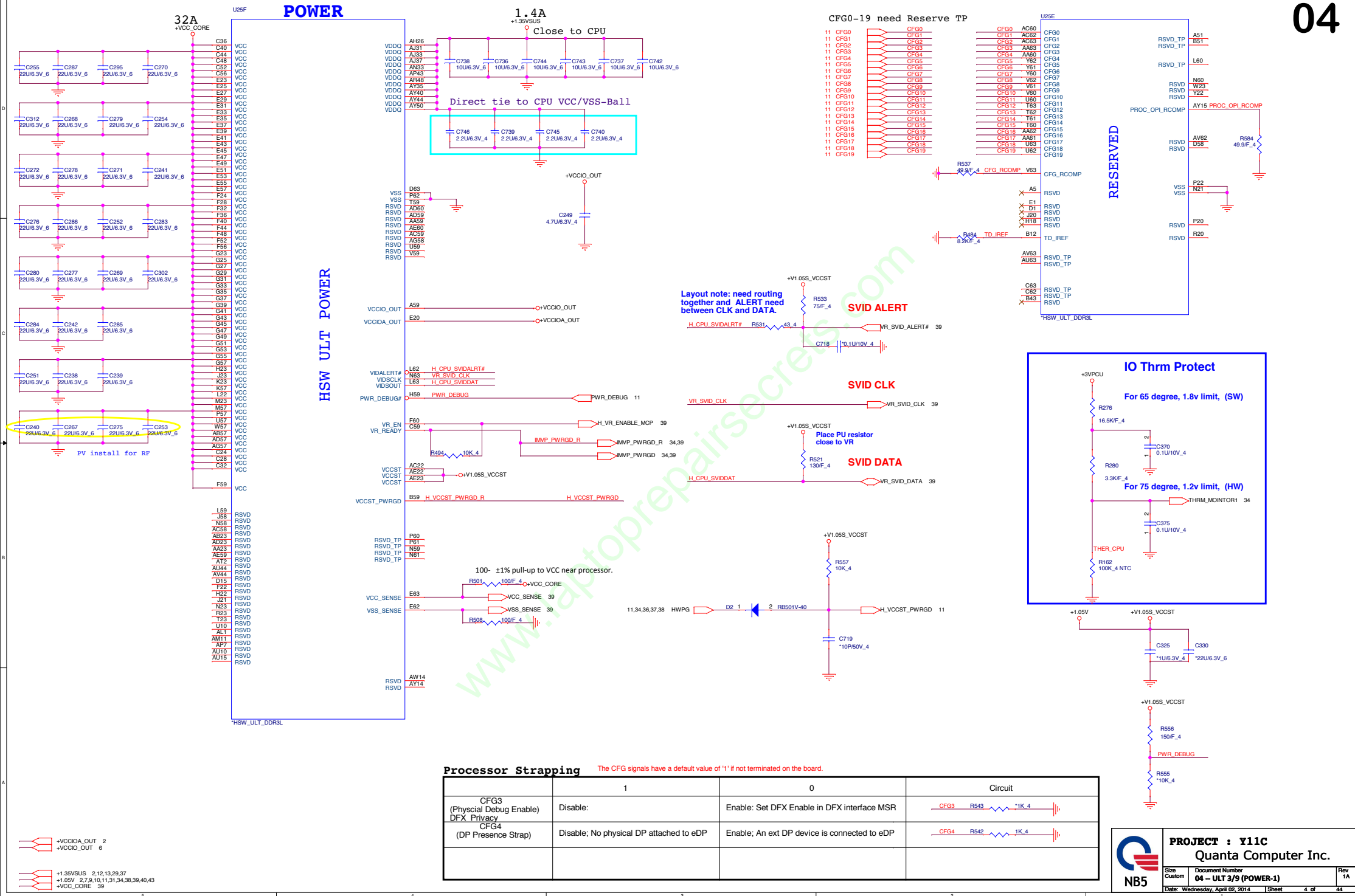
U25QC

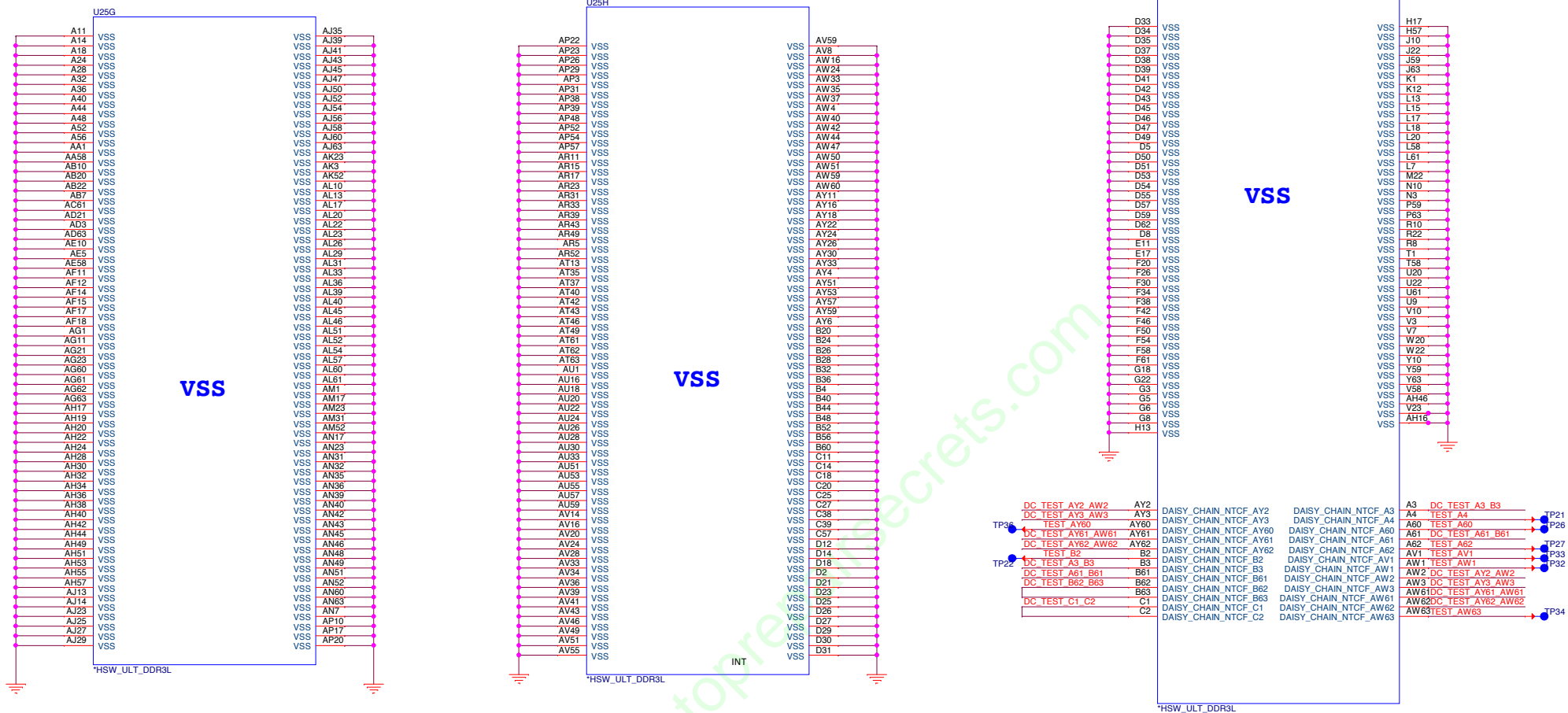
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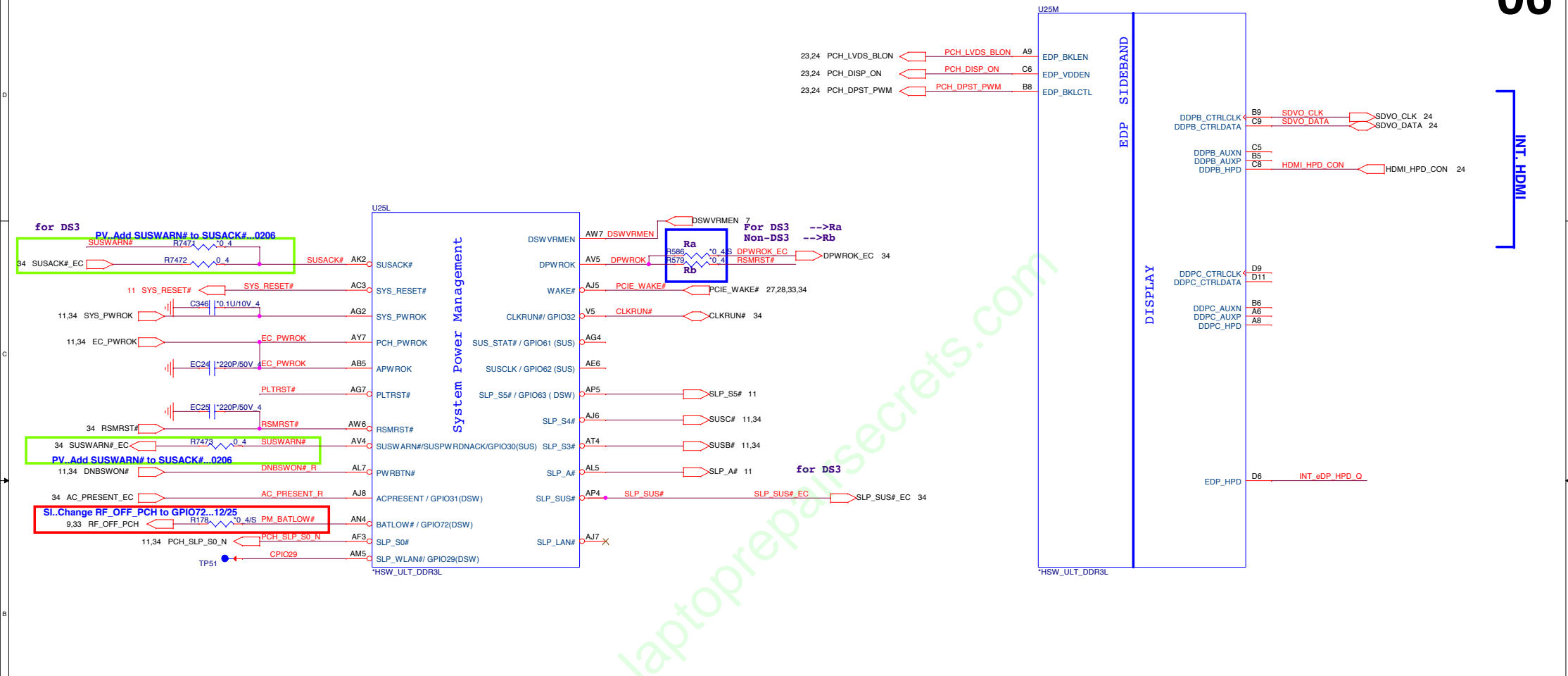
U25QE

U25QF

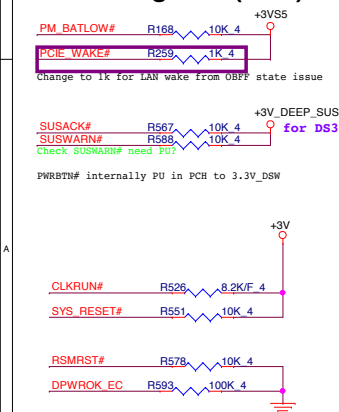
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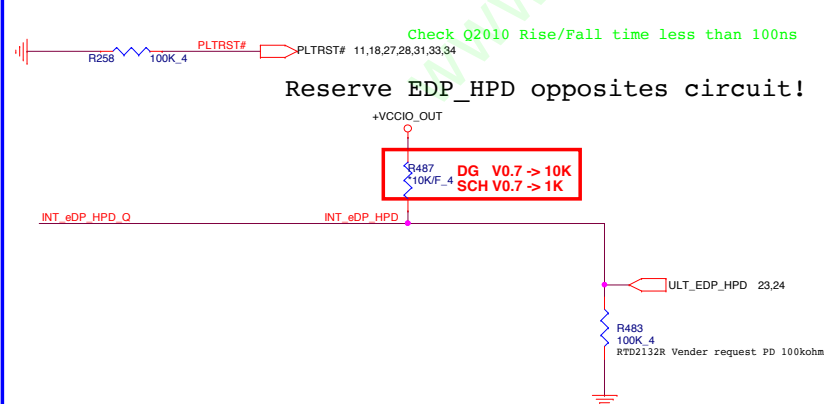




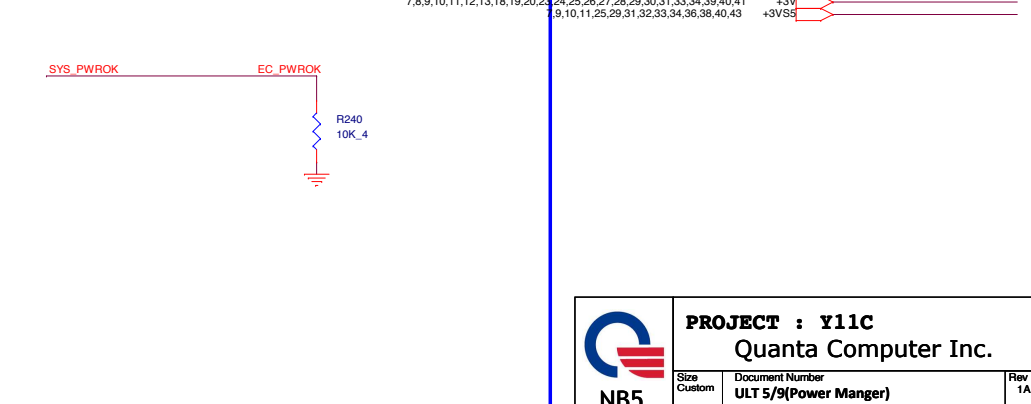
## PCH Pull-high/low(CLG)



## PLTRST#(CLG)



## System PWR\_OK(CLG)



## PCH Strap Table



**RTC Clock 32.768KHz**

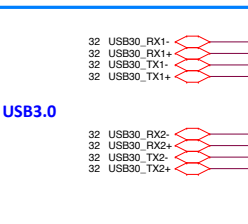
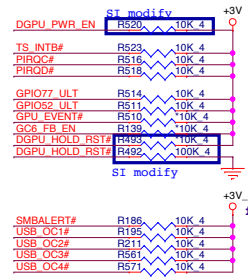


### 4M SPI ROM Socket

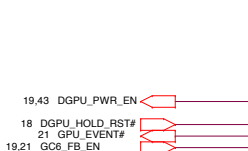


Lynx Point-LP Platform Controller Hub  
(HDA, JTAG, SATA)

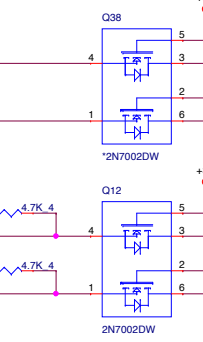
PCI/USBOC# Pull-up(CLG)



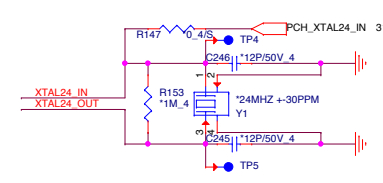
20111130 Modify USB3.0 for HM70



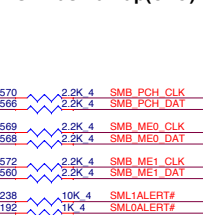
SMBus/Pull-up(CLG)



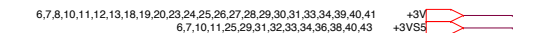
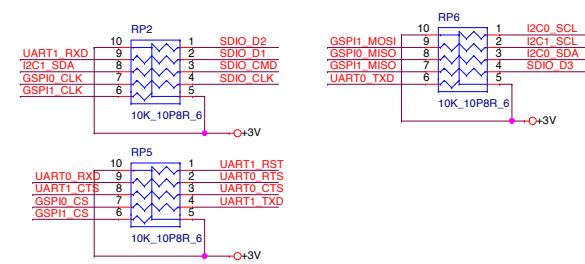
CLK\_REQ/Strap Pin(CLG)




SMBus/Pull-up(CLG)



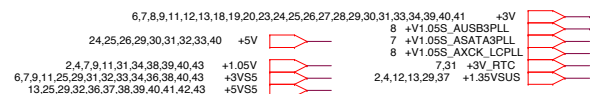
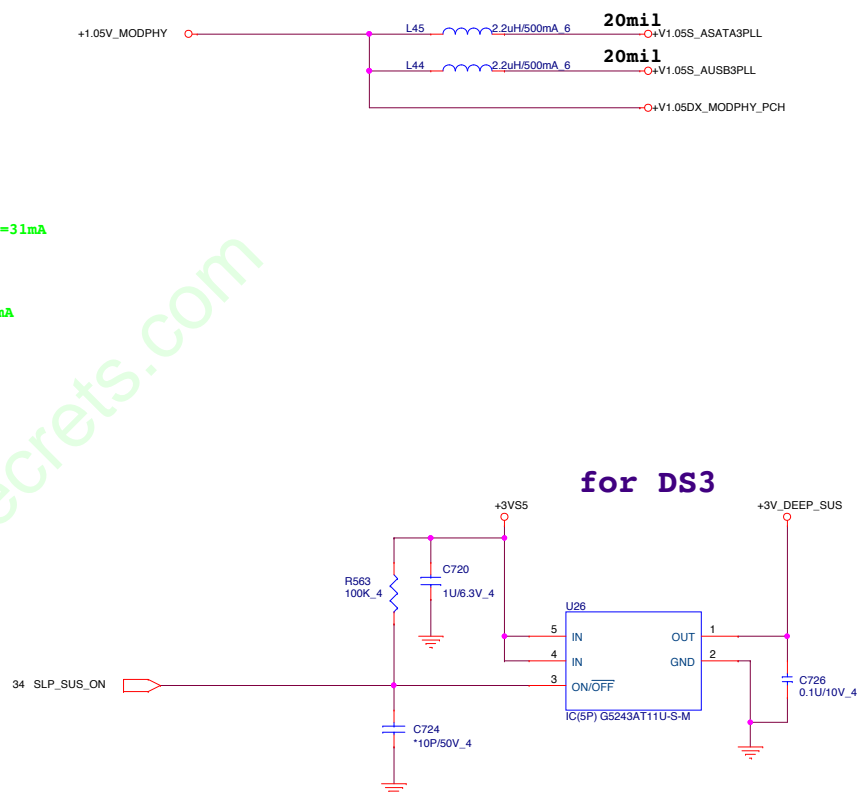
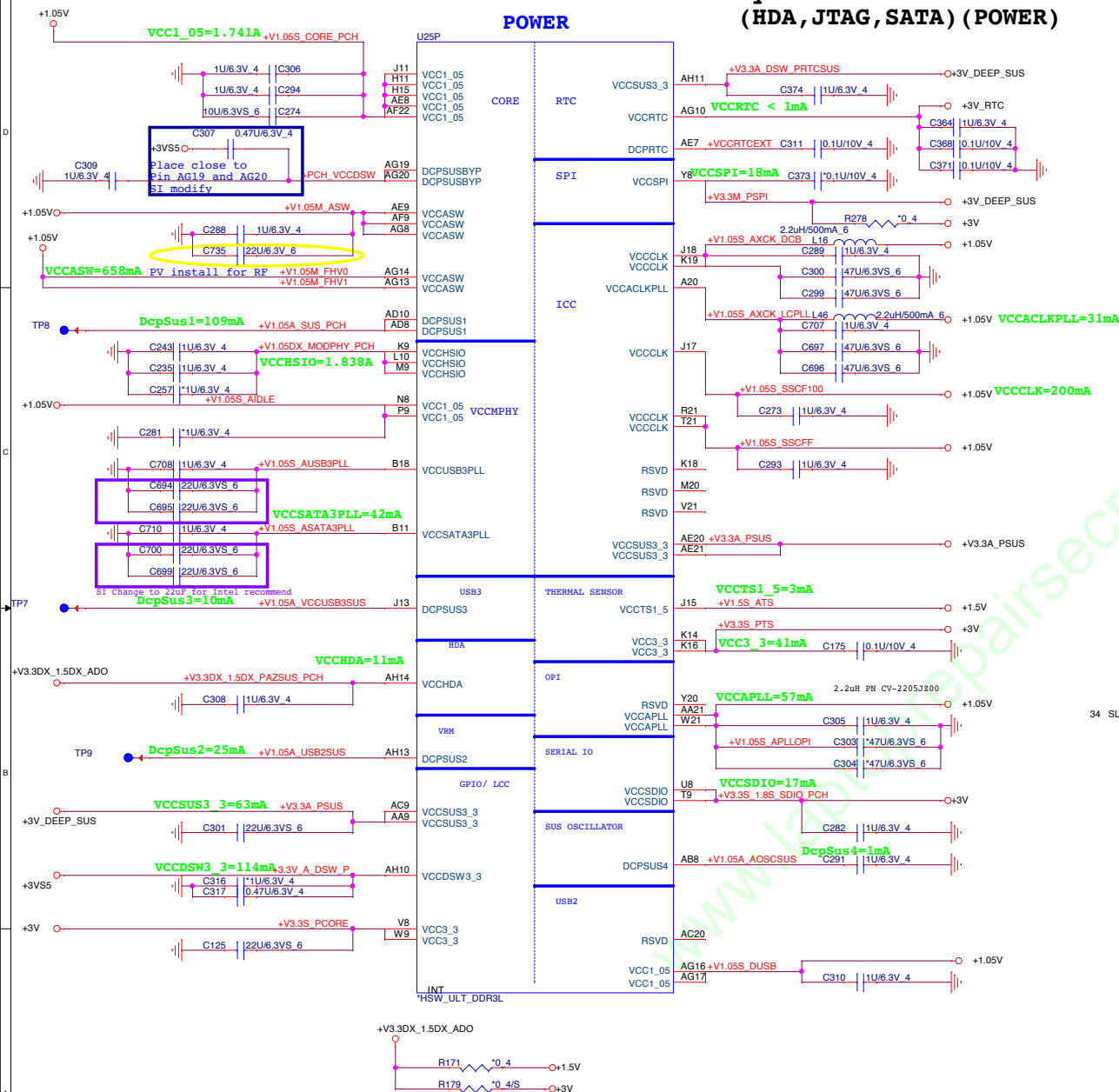
## 09

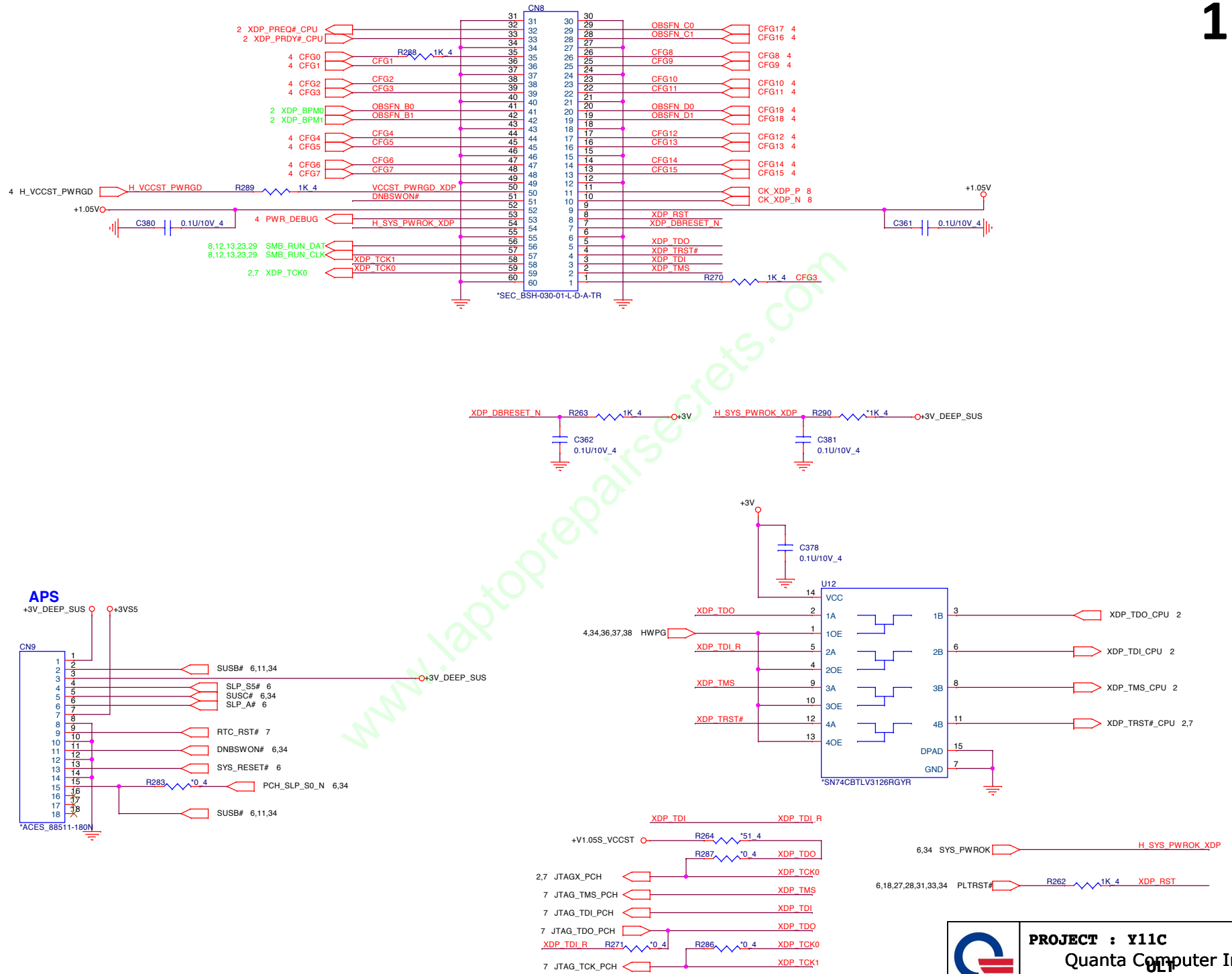


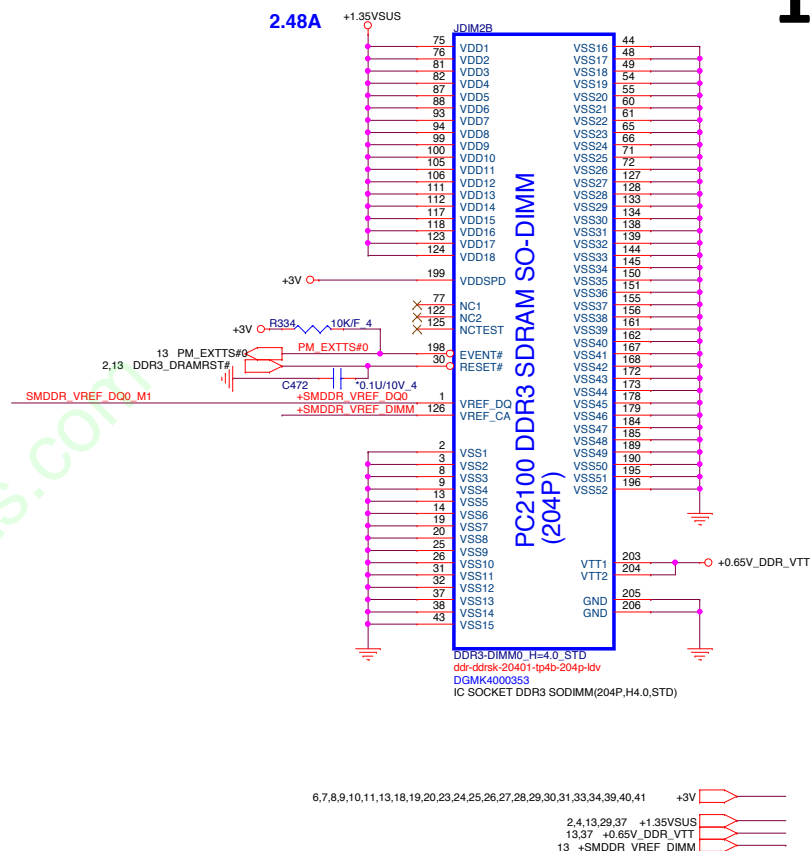
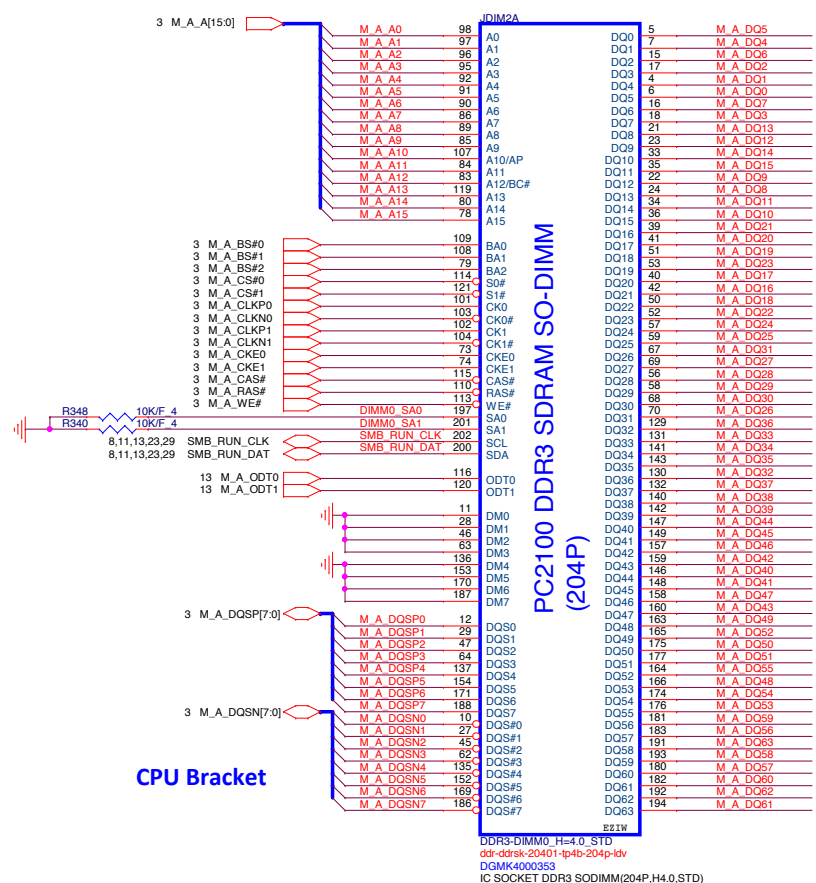
The diagram shows a 10-bit DAC resistor ladder. It consists of 10 resistors, each labeled "10K 4", connected in series between a 3V supply (labeled "+3V\_DEEP\_SUS") and ground. The nodes between the resistors are labeled BOARD\_ID0 through BOARD\_ID9. The output of the ladder is connected to a 3V supply.

 <b>NB5</b>	<b>PROJECT : Y11C</b> <b>Quanta Computer Inc.</b>		
	Size Custom	Document Number ULT 8/9 (GPIO/MISC)	Rev 1A
	Date: Wednesday, April 02, 2014	Sheet	9 of 44

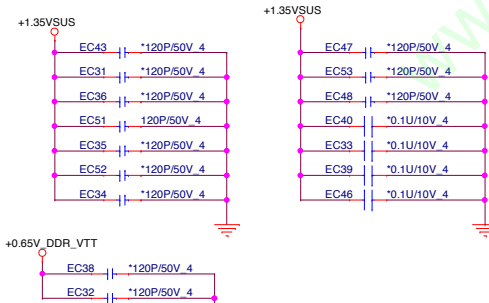
## Lynx Point-LP Platform Controller Hub (HDA, JTAG, SATA) (POWER)





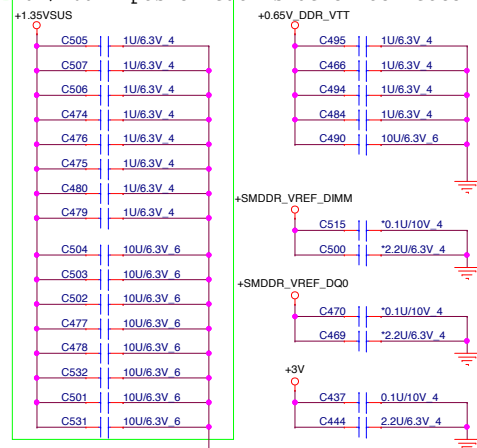


**For EMI RESERVE**

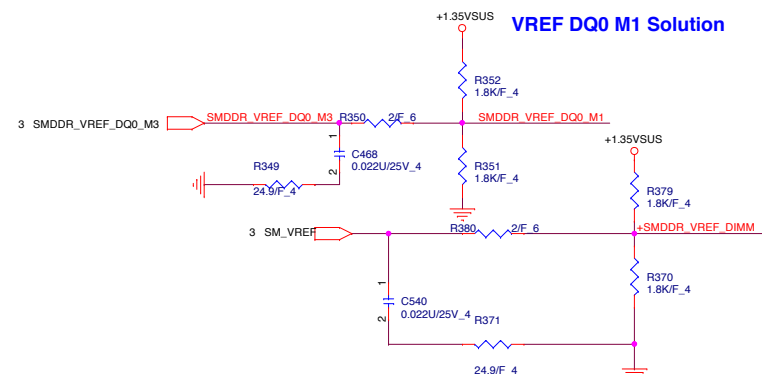


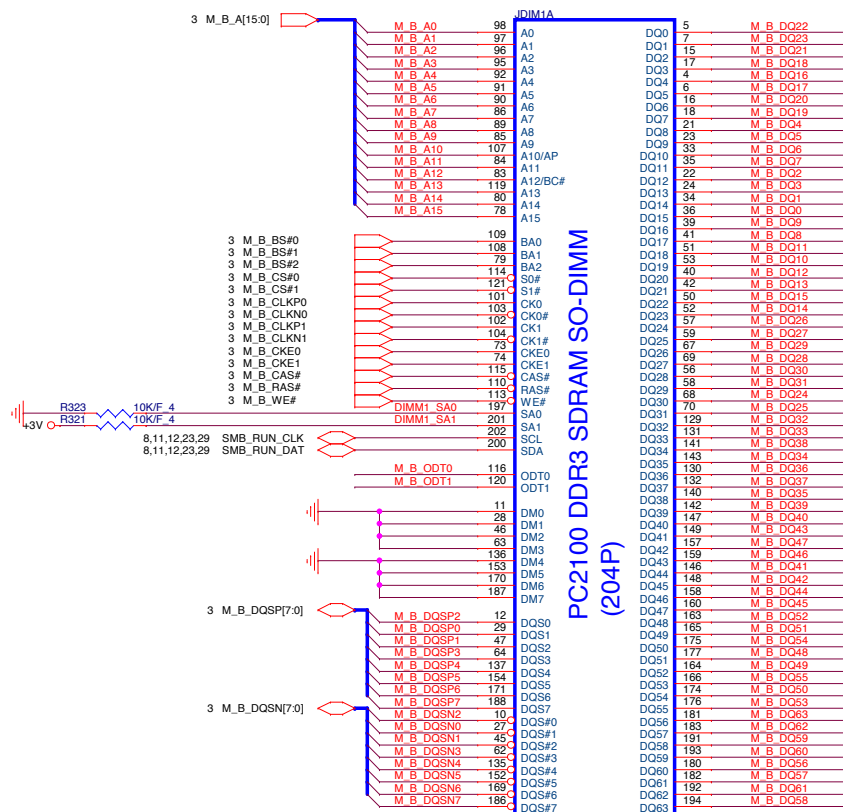
**Place these Caps near So-Dimm0.**

1uF/10uF 4pcs on each side of connector

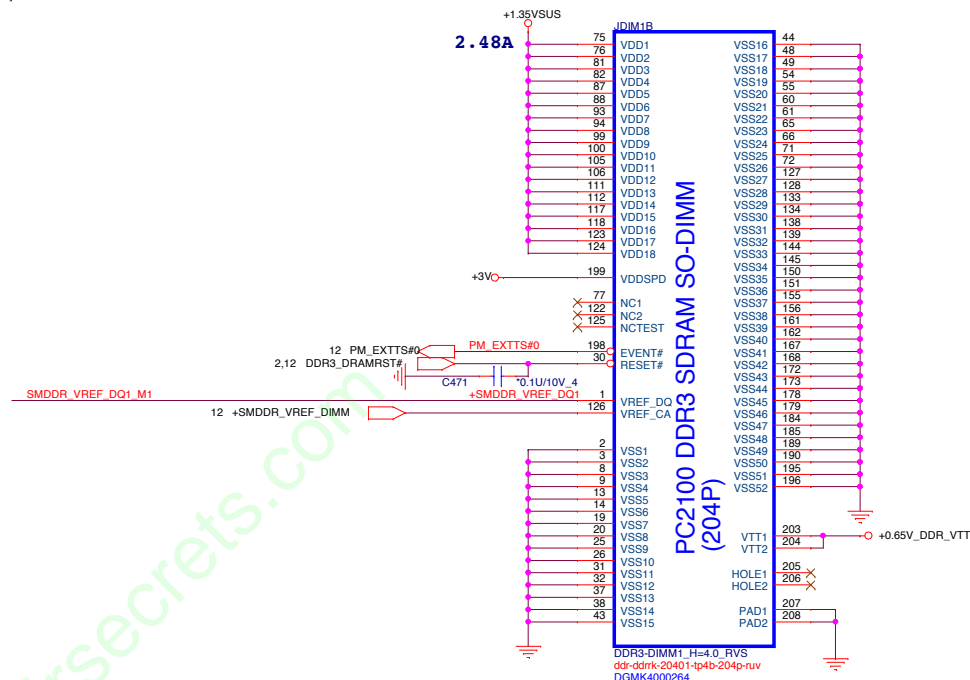


### VREF DQ0 M1 Solution



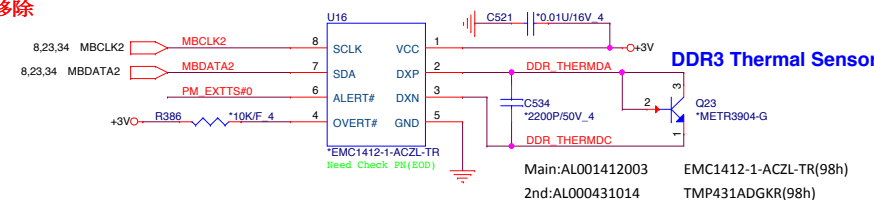


DDR3-DIMM1\_H=4.0\_RVS  
ddr-ddrrk-20401-tp4b-204p-ruv  
DGMK4000264

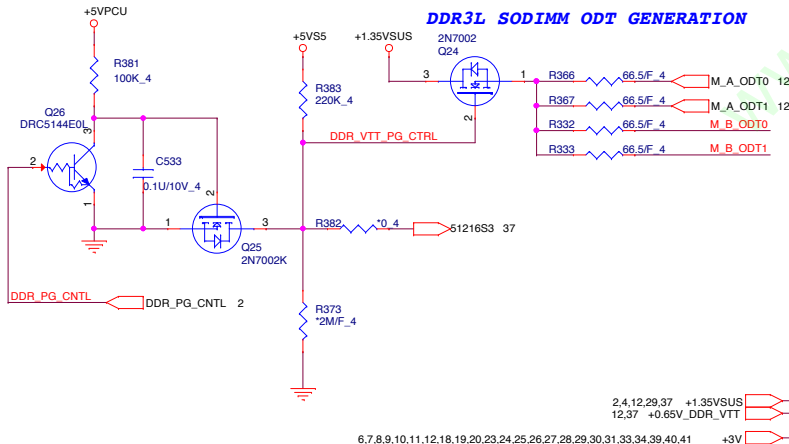


### Local Thermal Sensor

MV可移除

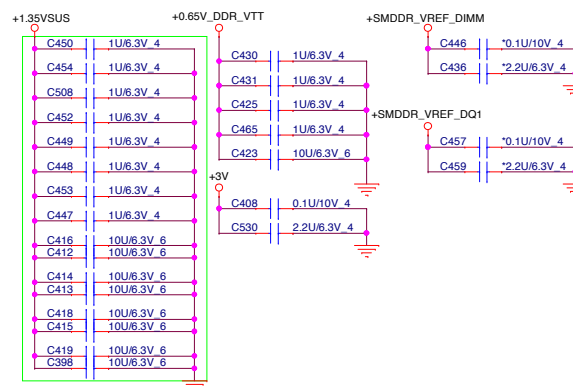


## DDR3L SODIMM ODT GENERATION

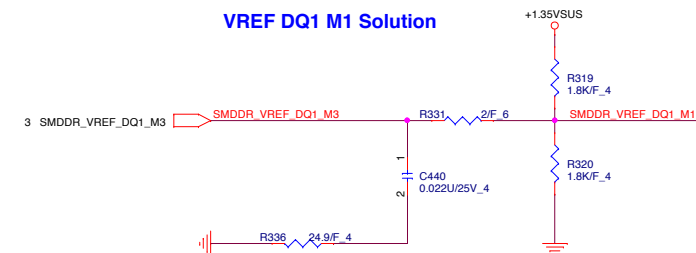


**Place these Caps near So-Dimm1.**

1uF/10uF 4pcs on each side of connector




### VREF DQ1 M1 Solution



**PROJECT : Y11C**  
Quanta Computer Inc.

Size Custom	Document Number <b>DDR3 DIMM1-RVS(4.0H)</b>	Rev 1A
Date: Wednesday, April 02, 2014	Sheet 13 of	44


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**PROJECT :Y05**  
Quanta Computer Inc.

Size Custom	Document Number <b>N15S-GT (PCIE VF) /NVDD</b>	Rev 2A
Date: Wednesday, April 02, 2014		Sheet 14 of 44


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**PROJECT :Y05**  
Quanta Computer Inc.

Size Custom	Document Number <b>N15S-GT (MEMORY/GND)</b>	Rev 2A
Date: Wednesday, April 02, 2014		Sheet 15 of 44

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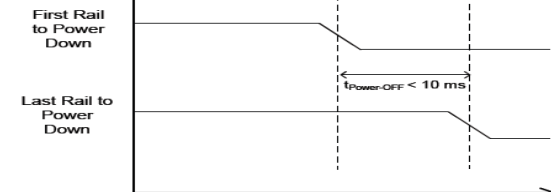
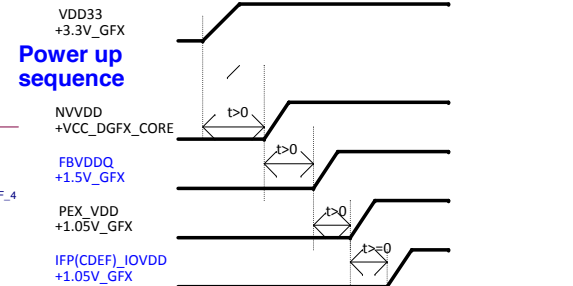
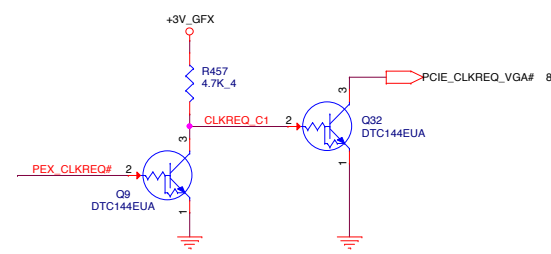
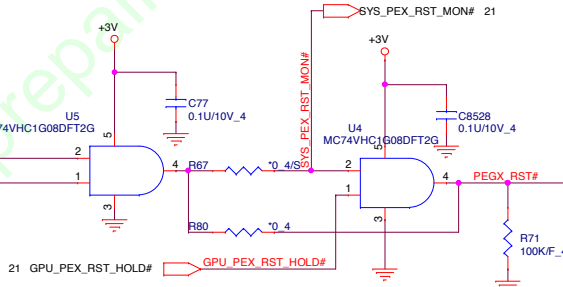
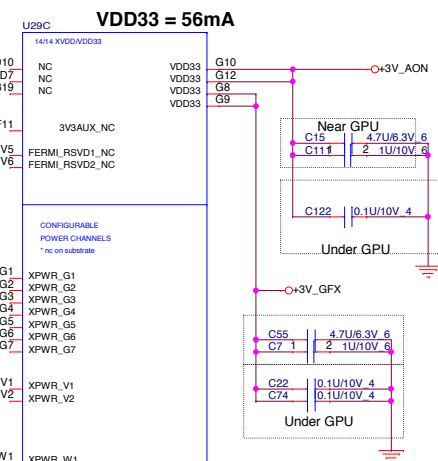
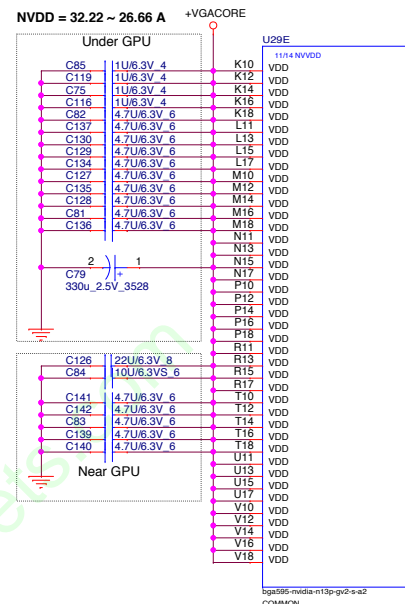
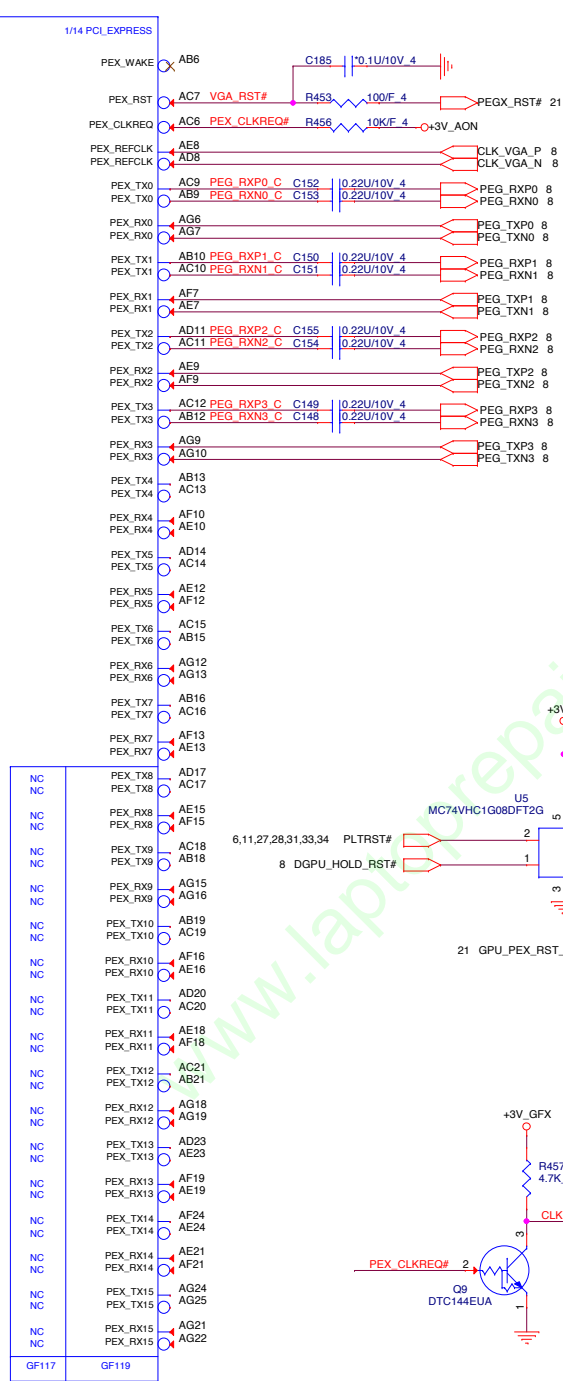
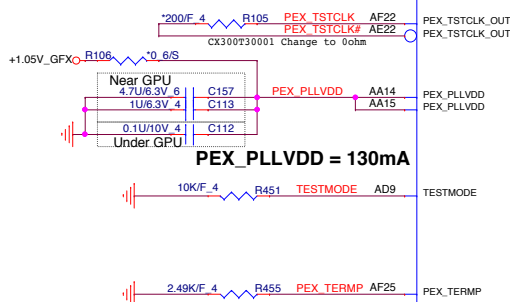
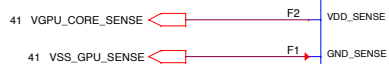
	<b>PROJECT :Y05</b> Quanta Computer Inc.	
	Size Custom	Document Number <b>N15S-GT (DISPLAY)</b>
	Date: Wednesday, April 02, 2014	Rev 2A
Sheet 16 of 44		

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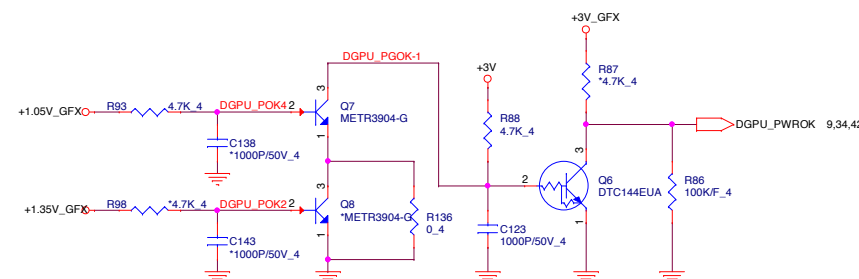
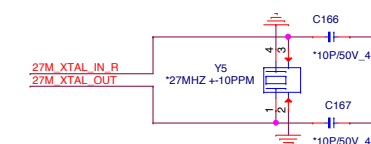
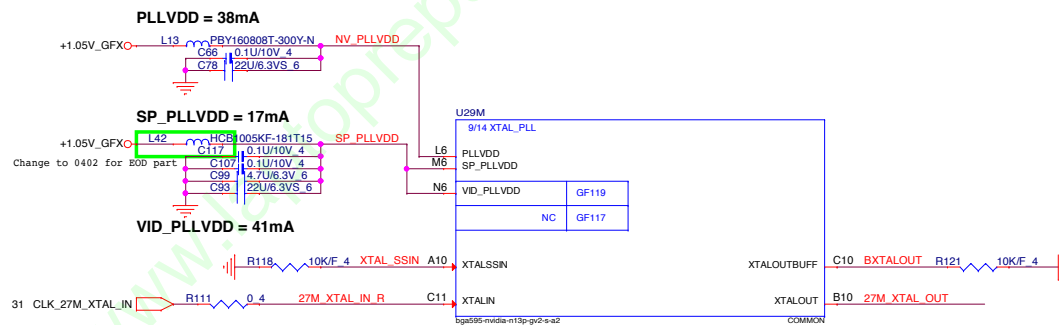
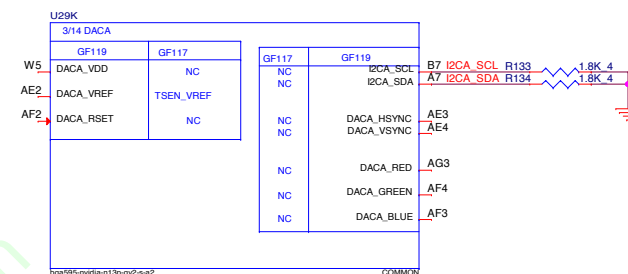
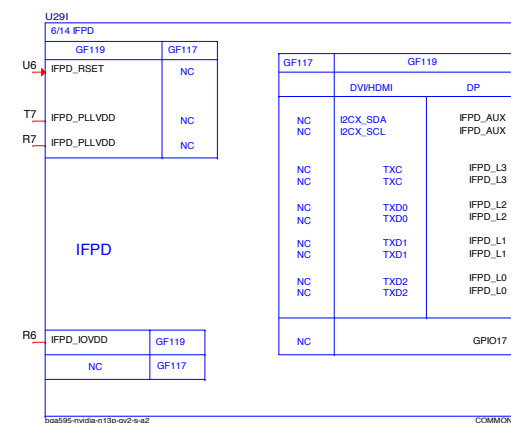
	<b>PROJECT :Y05</b> Quanta Computer Inc.		
	Size Custom	Document Number <b>N15S-GT (GPIO/STRAPS)</b>	Rev 2A
	Date: Wednesday, April 02, 2014		Sheet 17 of 44

+1.05V_GFX0			
C191	*22u/6.3V/5	A410	PEX_I/OVDD0
C186	*22u/6.3V/5	A412	PEX_I/OVDD0
C195	*10u/6.3V/5	A413	PEX_I/OVDD0
C187	*100u/3V/5	A416	PEX_I/OVDD0
C189	4.7u/6.3V/5	A418	PEX_I/OVDD0
Near GPU		A419	PEX_I/OVDD0
		A420	PEX_I/OVDD0
		A421	PEX_I/OVDD0
		AB22	PEX_I/OVDD0
		AC23	PEX_I/OVDD0
Under GPU		AD24	PEX_I/OVDD0
C141	*1u/6.3V/4	AE25	PEX_I/OVDD0
C144	*0.1u/10V/4	AF26	PEX_I/OVDD0
		AF27	PEX_I/OVDD0

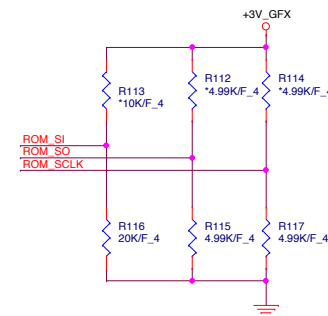
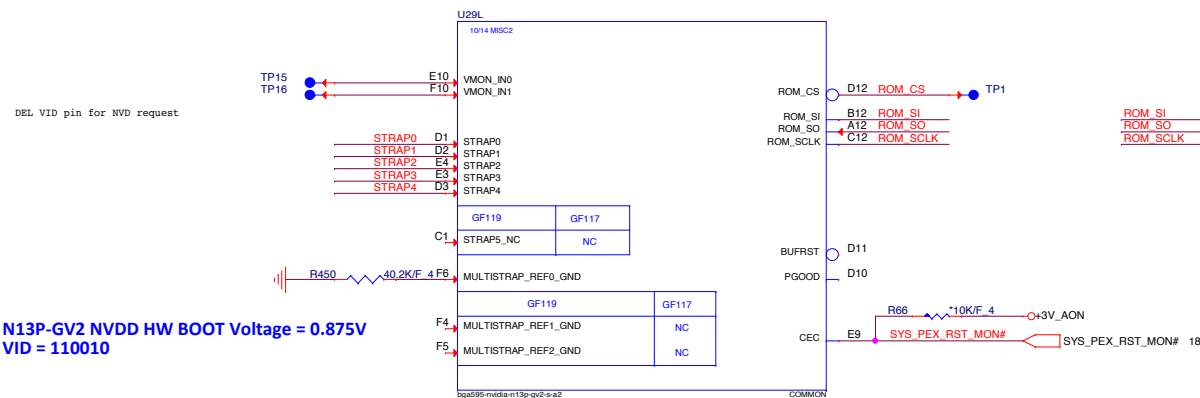
The schematic diagram shows the PLL and SVDD circuit. It features a +3V<sub>AON</sub> input and a ground connection. A dashed box labeled "Near GPU" contains three capacitors: C56 (10.1U/10V 4), C16 (4.7U/6.3V 6), and C14 (4.7U/6.3V 6). The circuit output is connected to pins AA8, AA9, and AB8. Pin AA8 is labeled PEX\_PLL\_HV1, pin AA9 is labeled PEX\_PLL\_HV2, and pin AB8 is labeled PEX\_SVDD\_3.



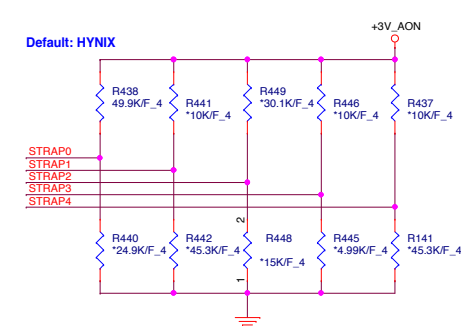




N13P-GV2 NVDD HW BOOT Voltage = 0.875V  
VID = 110010



Default: HYNIX

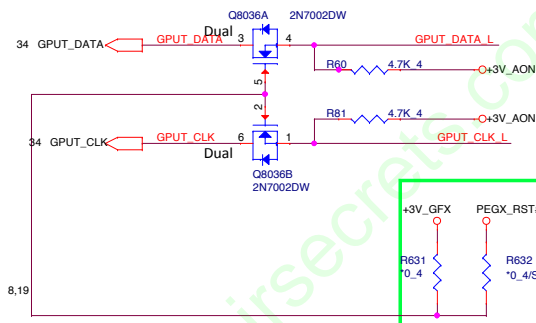
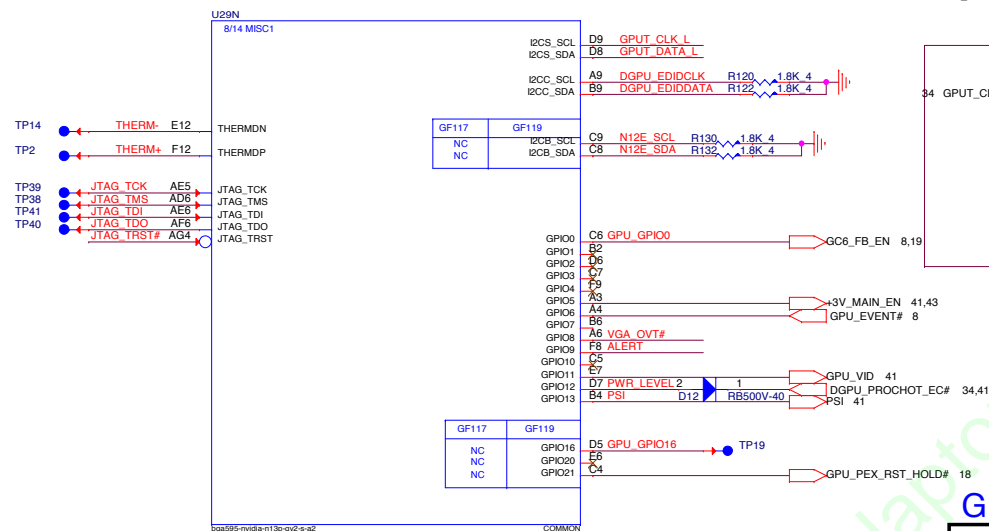


4.99k	CS24992FB26
10k	CS31002FB26
15k	CS31502FB24
20k	CS32002FB29
24.9k	CS32492FB16
30.1k	CS33012FB18
34.8k	CS33482FB22
45.3k	CS34532FB18

Table 15-2. Resistance Mapping to Hex Values

Resistor Values	Pull-Up to 3V3_MAIN	Pull-Down to GND
4.99 kΩ	1000	0000
10.0 kΩ	1001	0001
15.0 kΩ	1010	0010
20.0 kΩ	1011	0011
24.9 kΩ	1100	0100
30.1 kΩ	1101	0101
34.8 kΩ	1110	0110
45.3 kΩ	1111	0111

Hynix should be 0x3, R440 20K 1%  
Micro Should be 0x4, R440 24.9K 1%  
Samsung Should be 0x5, R440 30.1K 1%

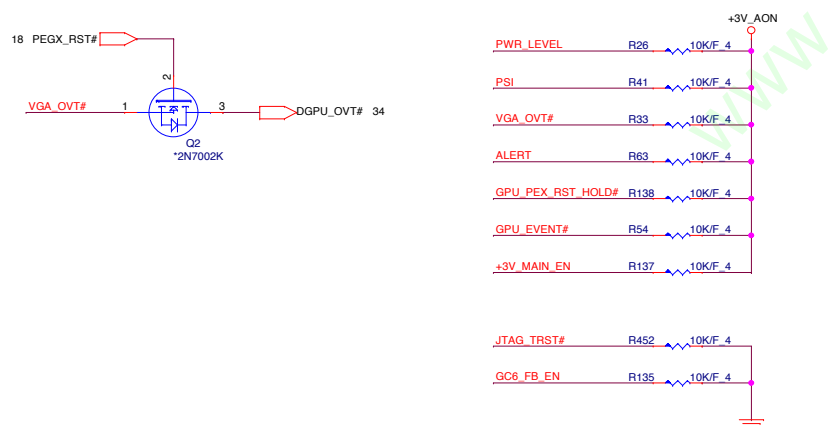


VRAM Configuration Table ROM\_SI

RAMCFG [3:0]	DESCRIPTION	Vendor	Vendor P/N	QCI P/N	QBC	TOP B/S
0000	DDR3 256Mx16, 64bit, 4Gb,900MHz	...	MT41J256M16HA-093G:E	S-M	AKD5PZSTL01	AKD5PZSTL00
0100	DDR3 256Mx16, 64bit, 4Gb,900MHz	Micron	H5TC4G63AFR-11C	AKD5PGWT500	AKD5PGWTW08	AKD5PGWTW07
0011	DDR3 256Mx16, 64bit, 4Gb,900MHz	HYNIX	4W4G1646D-BC1A			
0101		SAMSUNG				

## GPIO ASSIGNMENTS

GPIO	I/O	PIN	USAGE
0	IN	FB_CLAMP_MON	FB Clamp monitor
1	OUT	MEM_VDD_CTL	Memory VDD VID
2	OUT	LCD_BL_PWM	Panel Backlight PWM
3	OUT	LCD_VCC	PANEL POWER ENABLE
4	OUT	LCD_BLEN	PANEL BACKLIGHT ENABLE
5	OUT	Reserved	--
6	OUT	FB_CLAMP_TGL_REQ	Active low FB Clamp toggle request
7	OUT	3D VISION	3D VISION LEFT/RIGHT signal
8	I/O	OVERT	ACTIVE LOW THERMAL OVER TEMP
9	I/O	ALERT	ACTIVE LOW THERMAL ALERT
10	OUT	MEM_VREF_CTL	MEMORY VREF CONTROL
11	OUT	PWR_VID	GPU CORE_VDD PWM Control signal
12	IN	PWR_LEVEL	AC Power detect or power supply overdraw input
13	OUT	PSI	Phase Shedding

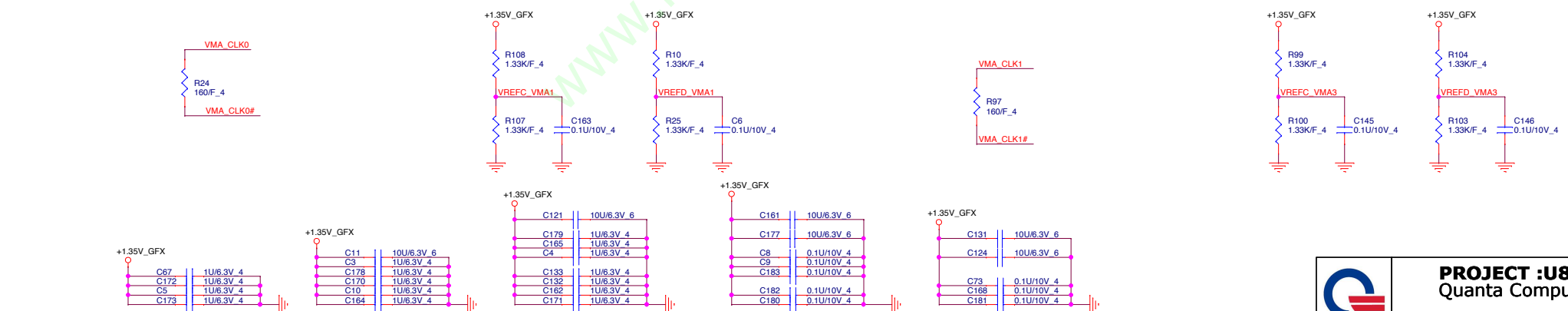
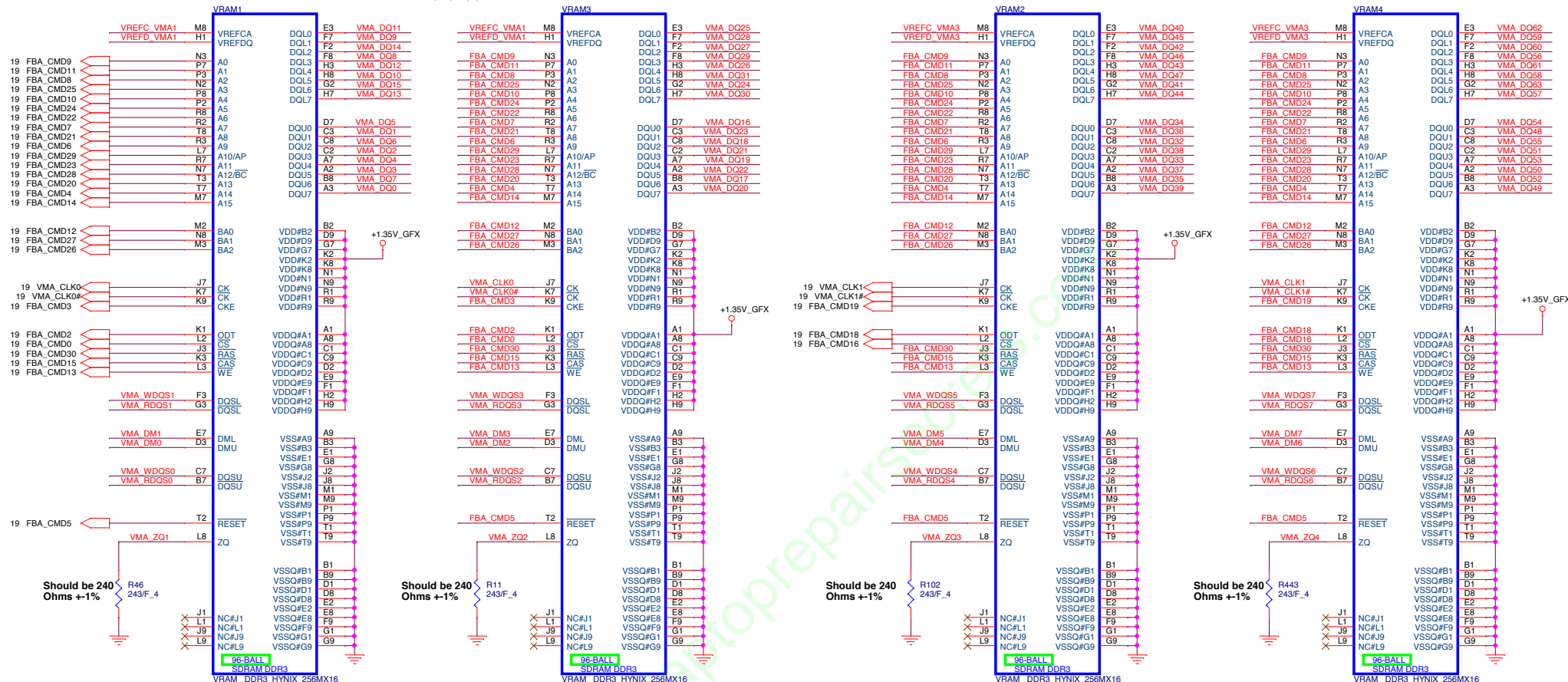


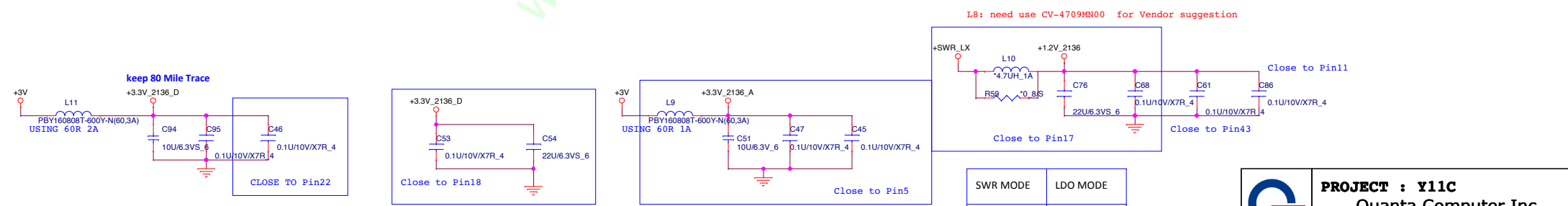
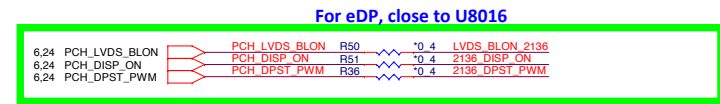
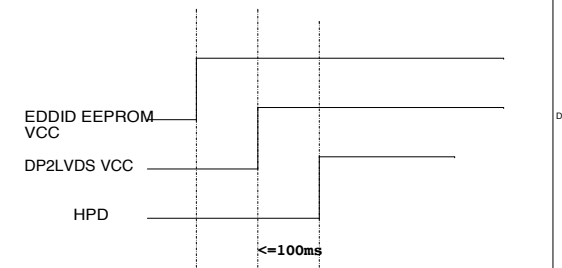
# CHANNEL A: 256MB/512MB DDR3

22


HYU 256Mx16, PN : AKD5PGWTW08---AKD5PGWTW07  
HYU 128Mx16, PN : AKD5MZDTW03---AKD5MZDTW02  
SAM 256Mx16, PN : AKD5PZDT501---AKD5PZDT500  
SAM 128Mx16, PN : AKD5MGGT535---AKD5MGGT534

19 VMA\_DQ[63..0]  
19 VMA\_DM[7..0]  
19 VMA\_WDQS[7..0]  
19 VMA\_RDQS[7..0]

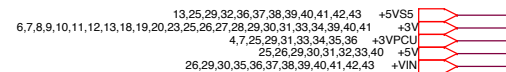
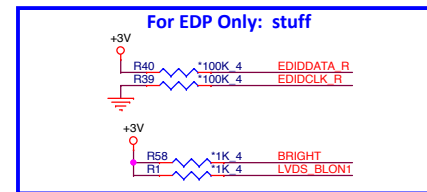
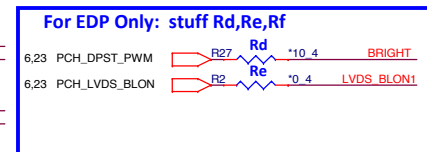
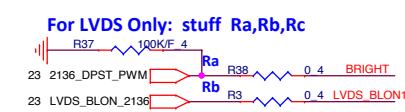
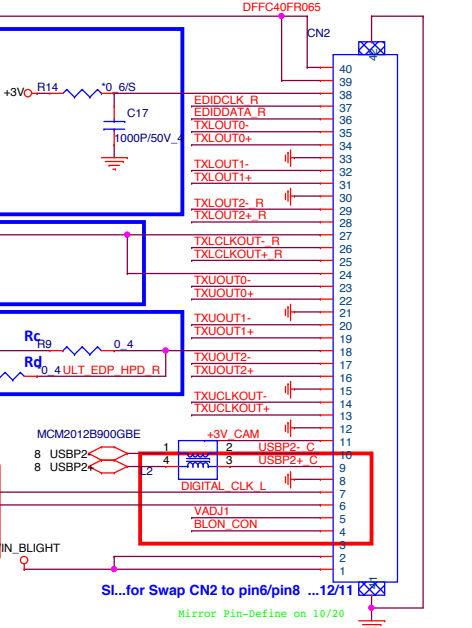
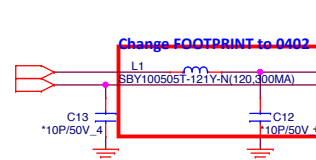
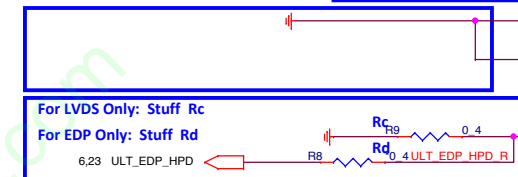
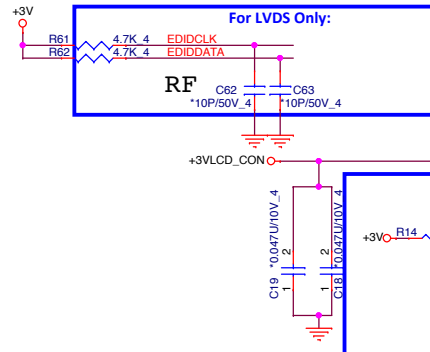
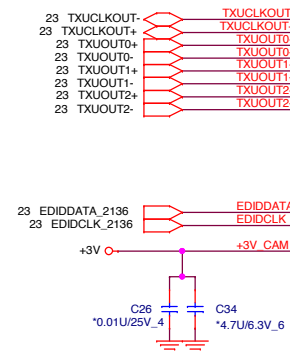
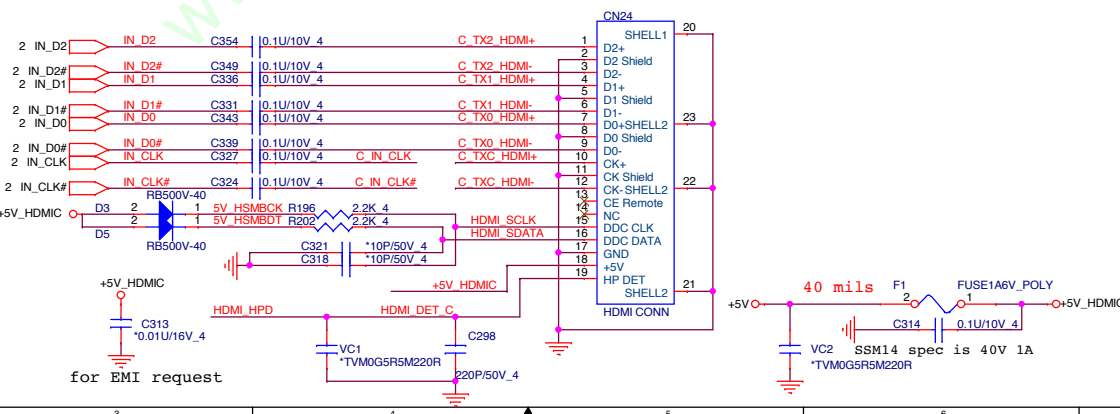
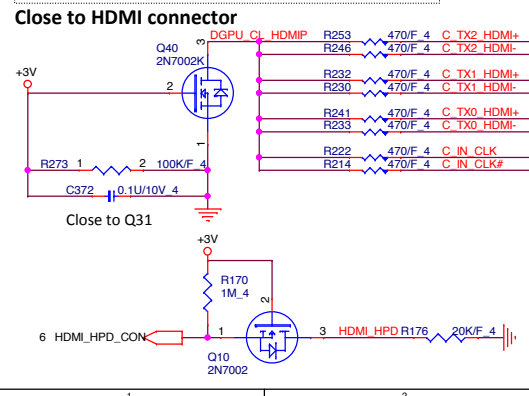
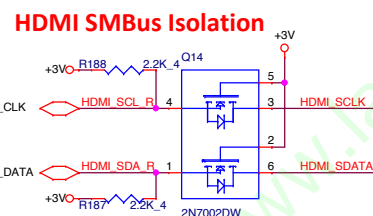
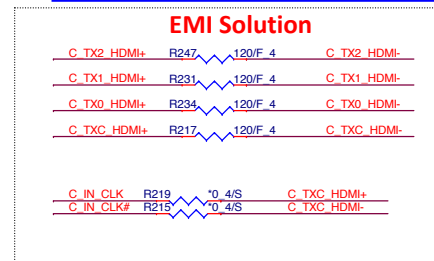
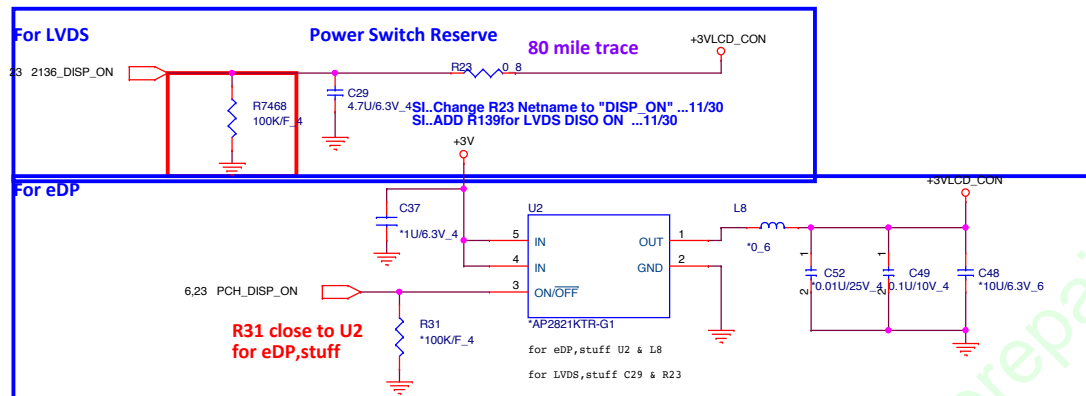
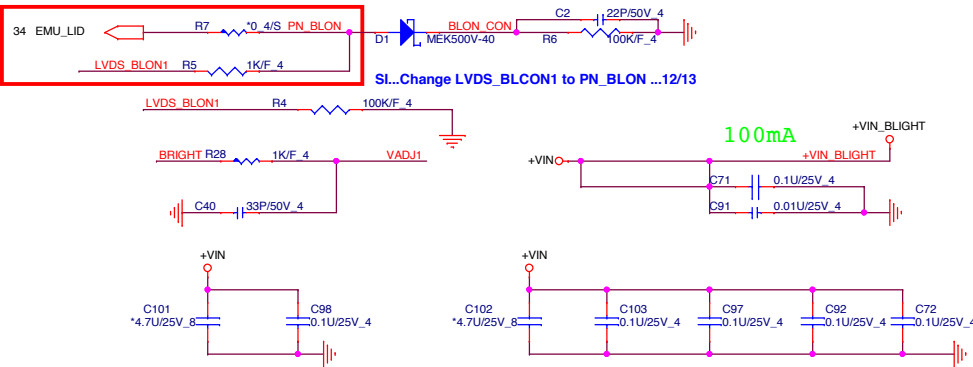


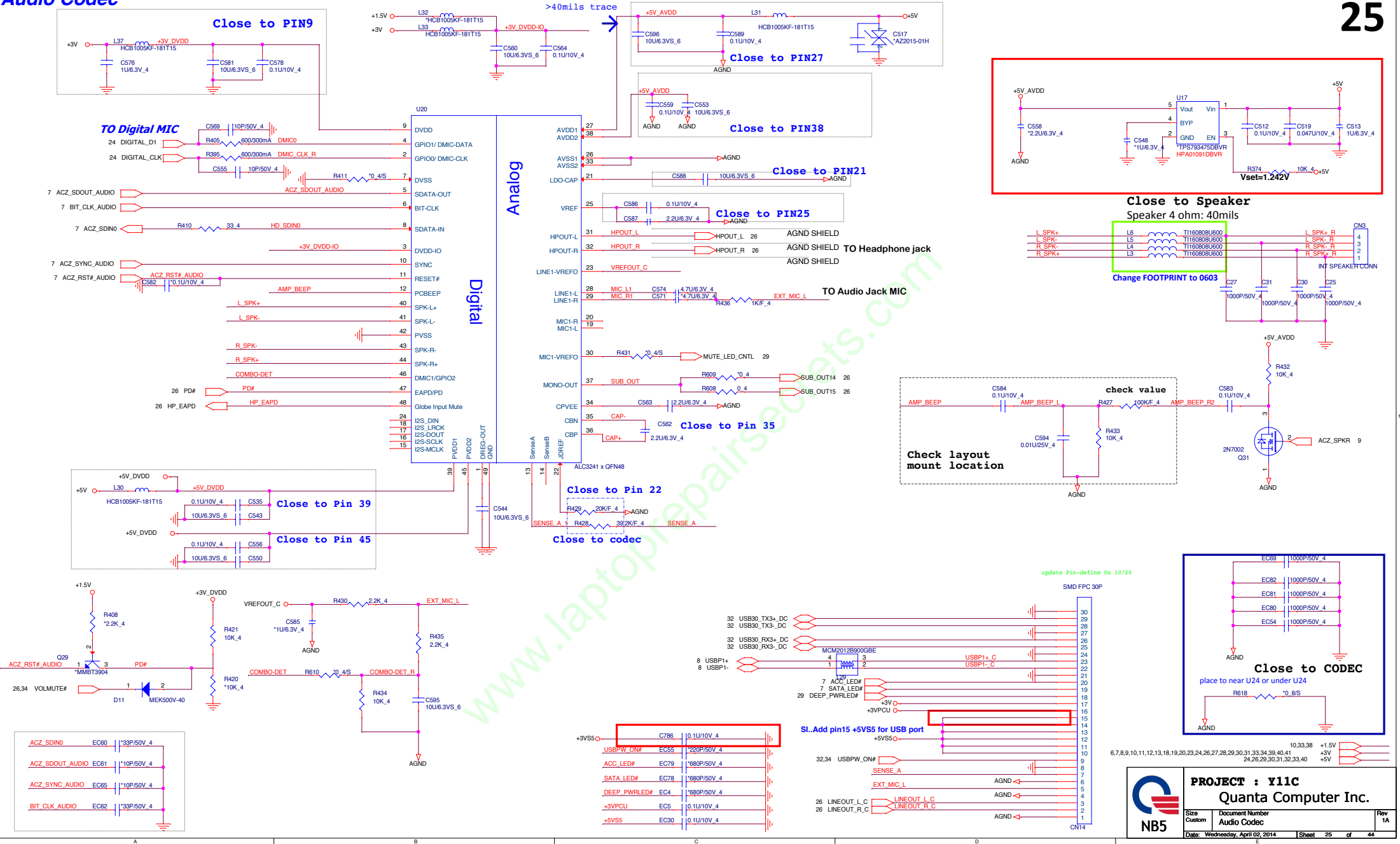


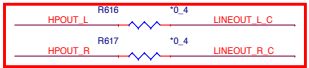
SWR MODE	LDO MODE
Stuff L8	Stuff R86

	<b>PROJECT : Y11C</b> <b>Quanta Computer Inc.</b>		
	Size Custom	Document Number <b>RTD2136</b>	Rev 1A
	Date: Wednesday, April 02, 2014      Sheet 23 of 44		

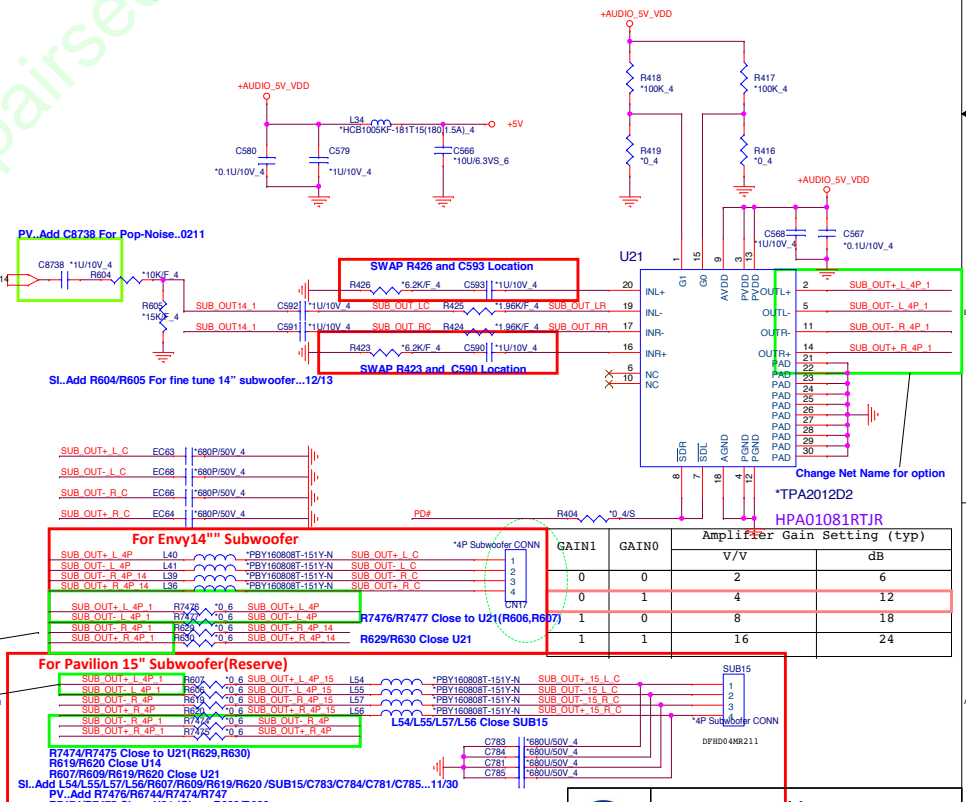
## LID Switch





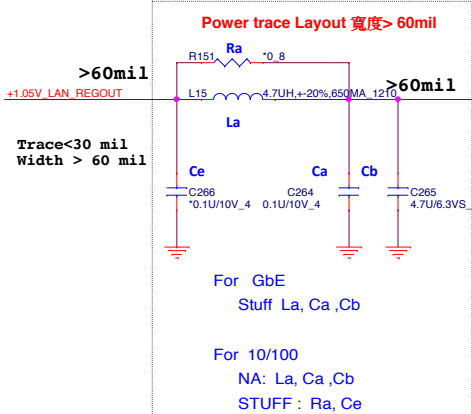
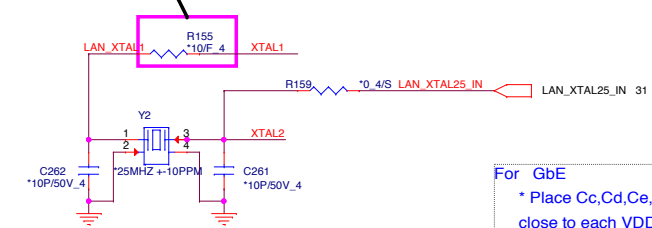


**For Pavilion 15" Subwoofer(Reserve)  
For Envy14"" Subwoofer**



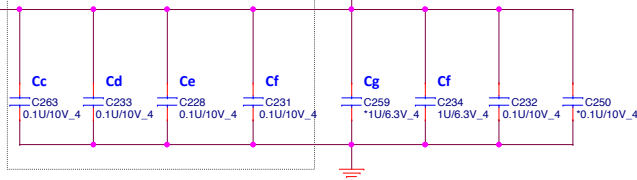
GAIN1	GAIN0	dB
0	0	20
0	1	26
1	0	32
1	1	36

For EMI 0 ~ 22 ohm



For GbE  
\* Place Cc, Cd, Ce, Cf, C232  
close to each VDD10 pin-- 3, 22, 8, 30

For 10/100 NA Ce, Cf  
\* Place Ce, Cf, C250  
close to each VDD10 pin-- 8, 30 only,

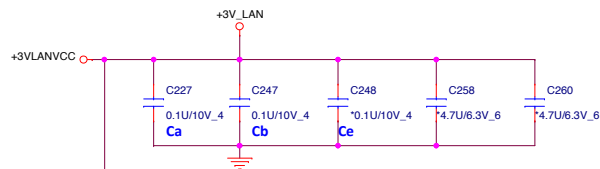


For GbE  
\* Place Cf close to each VDD10 pin-- 22 (reserve)

For 10/100  
\* Place Cg close to each VDD10 pin-- 30 (reserve)

For 10/100  
\* Stuff Cb and Ce only, close to each VDD33 pin-- 23, 32

For GIGA  
\* Stuff Ca and Cb only, close to each VDD33 pin-- 11, 32

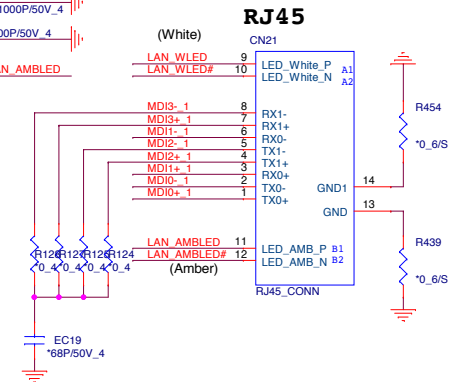
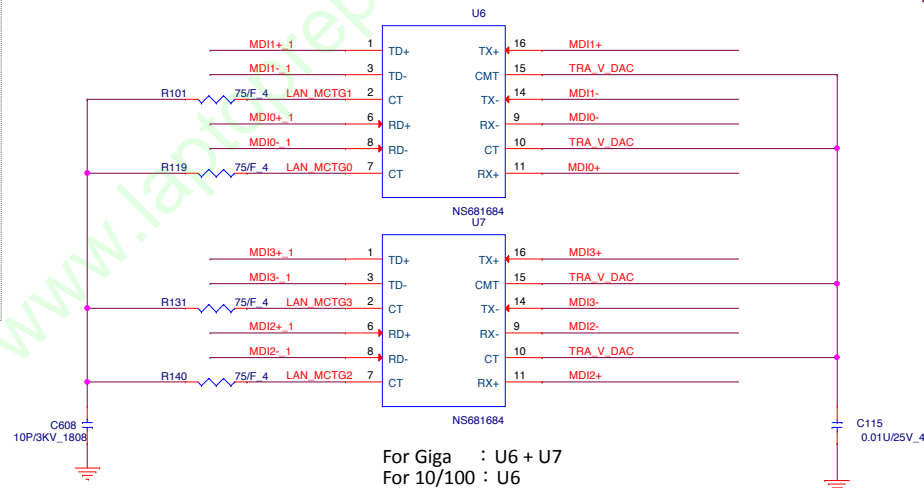
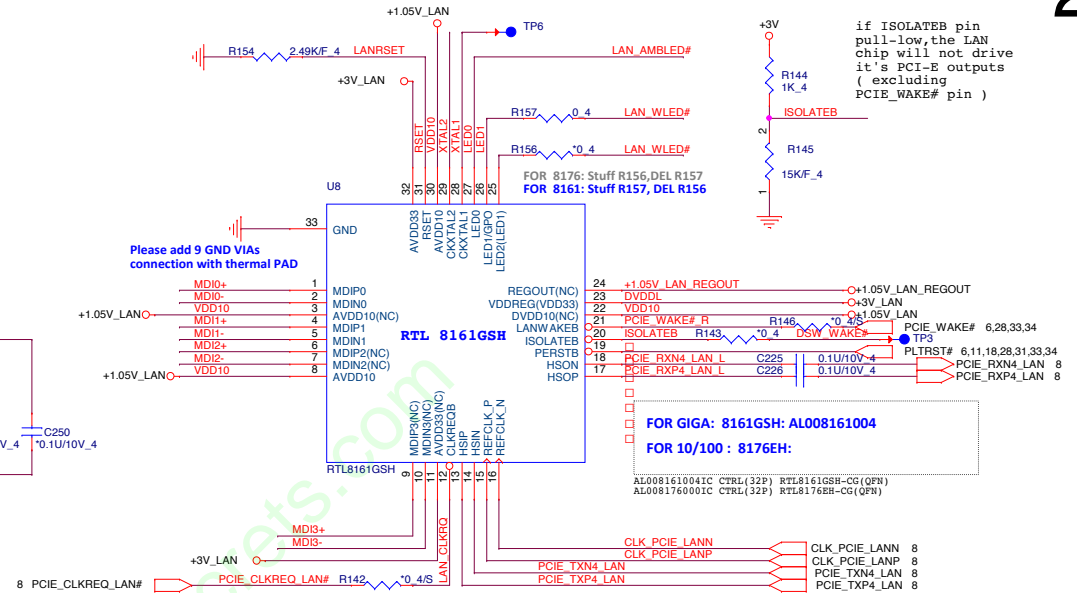


\* Place Cc and Cd close to each VDD33 pin-- 23

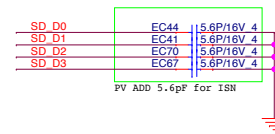
For GIGA  
Stuff Cc, Cd

For 10/100  
NA: Cc, Cd

Remove For Not Using SWR mode



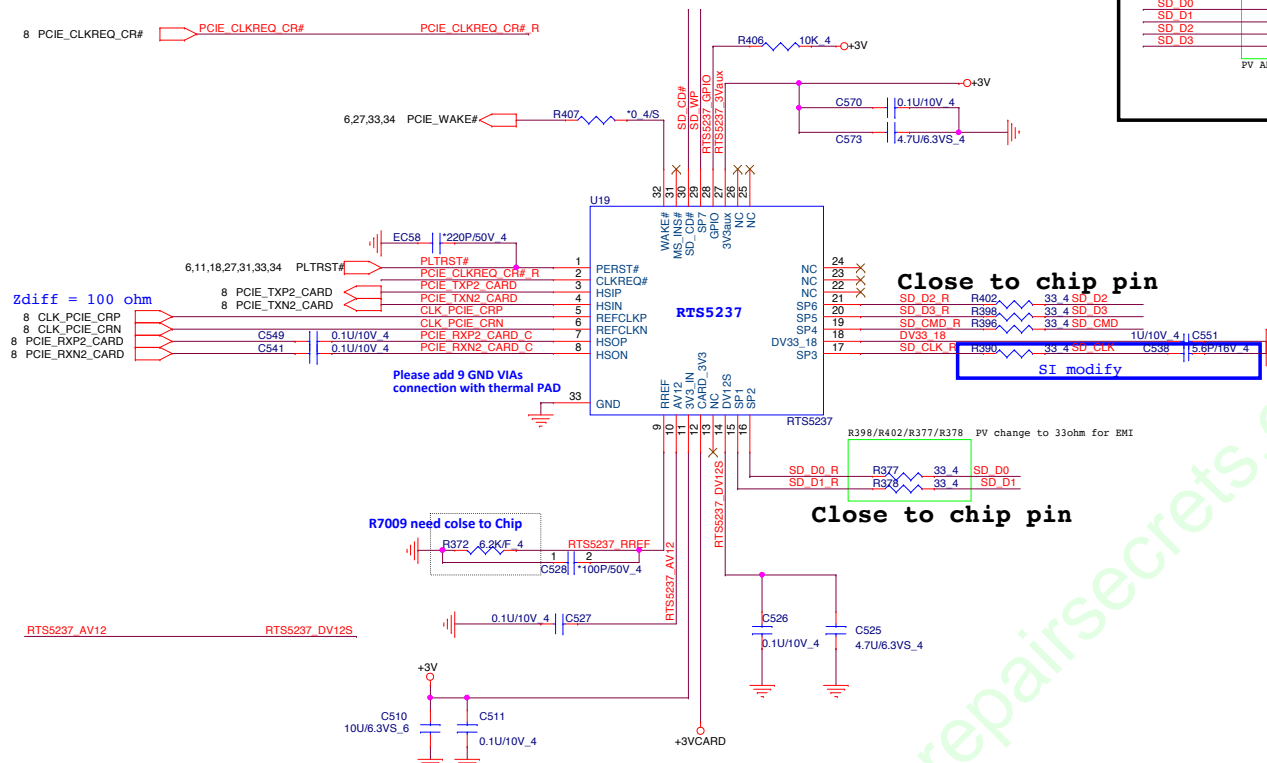
Reserve for EMI



SP1	SD D1	
SP2	SD D0	MS D0
SP3	SD CLK	MS D1
SP4	SD CMD	MS D2
SP5	SD D3	MS D3
SP6	SD D2	MS CLK
SP7	SD WP	MS BS

## Share Pin

SD / MMC



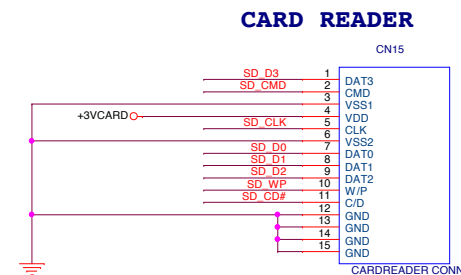
NC 24 ✗  
NC 23 ✗  
NC 22 ✗

**Close to chip pin**

SI modify

Close to chip pin

CLOSE CONN

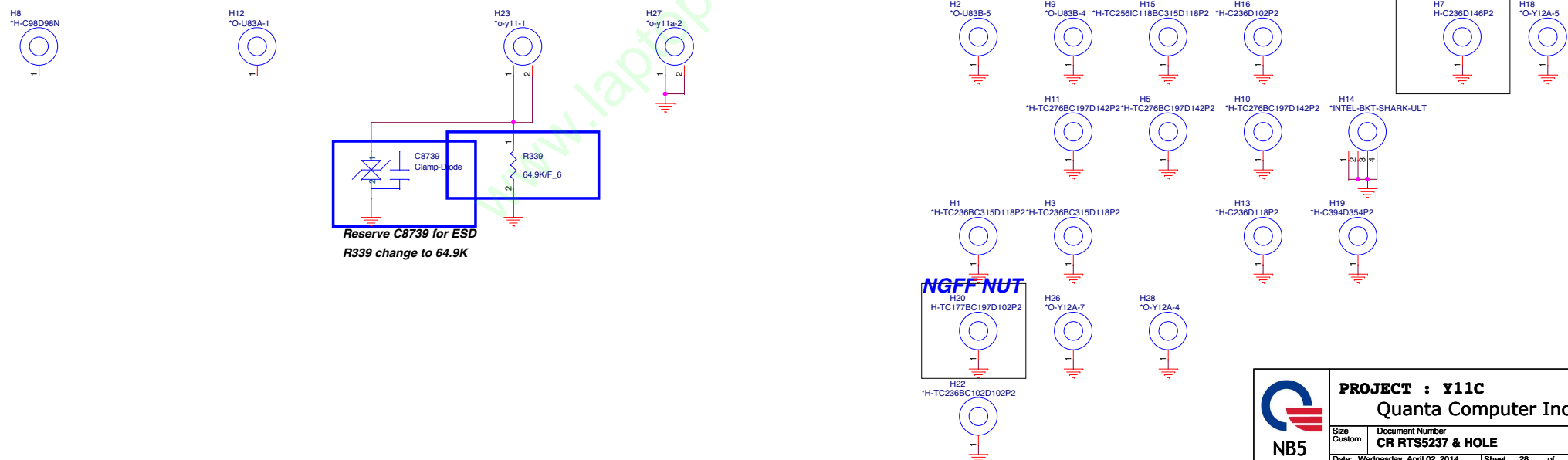


**CARD READER**

CN15

R6x Type

## Thermal Nut



**Reserve C8739 for ESD**  
**R339 change to 64.9K**

**NGFF NUT**

H20  
H-TC177BC197D102P2



H22

\*H-TC236BC102D102P2

(c)

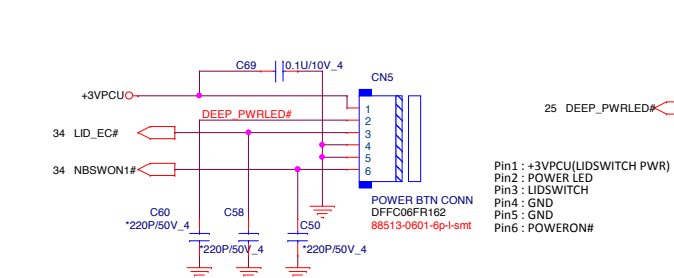


**PROJECT : Y11C**  
Quanta Computer Inc.

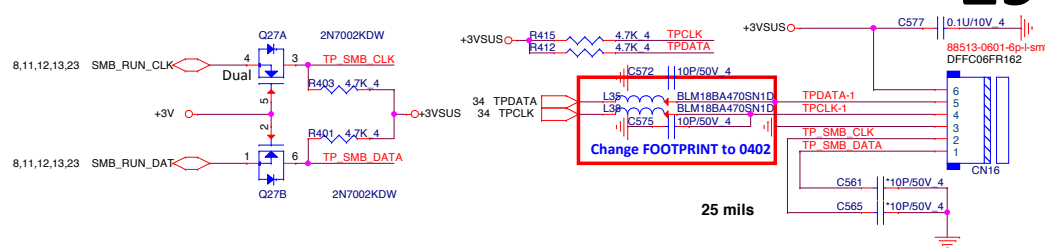
Size Custom	Document Number <b>CR RTS5237 &amp; HOLE</b>
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Date: Wednesday April 02 2014 Sheet 28 of 44

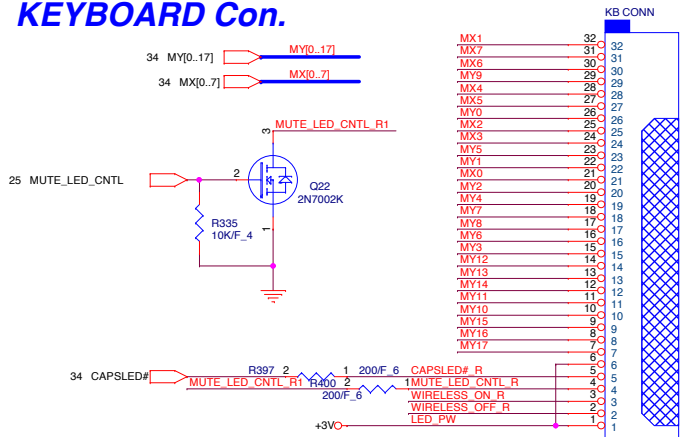
Power Button Connector



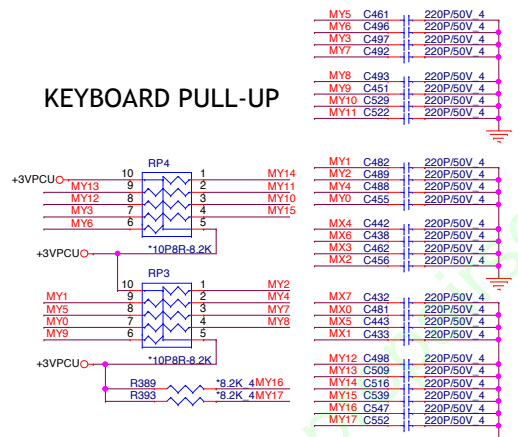
Touch Pad Connector



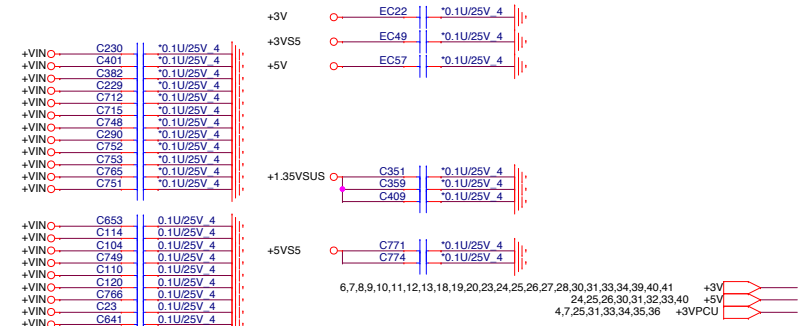
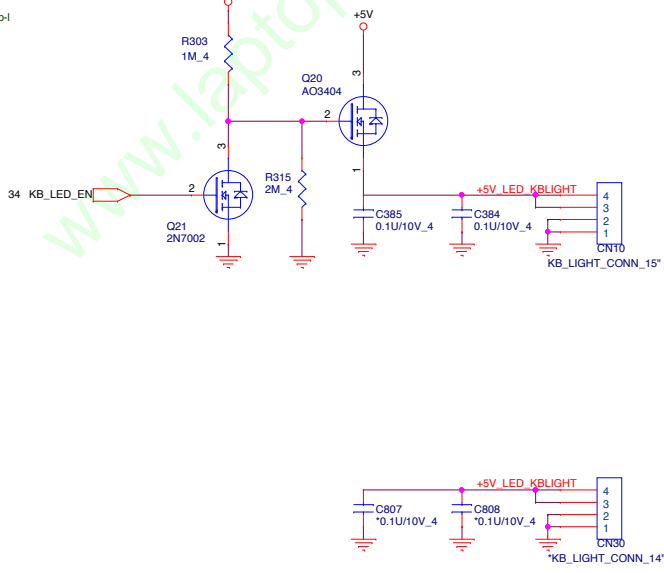
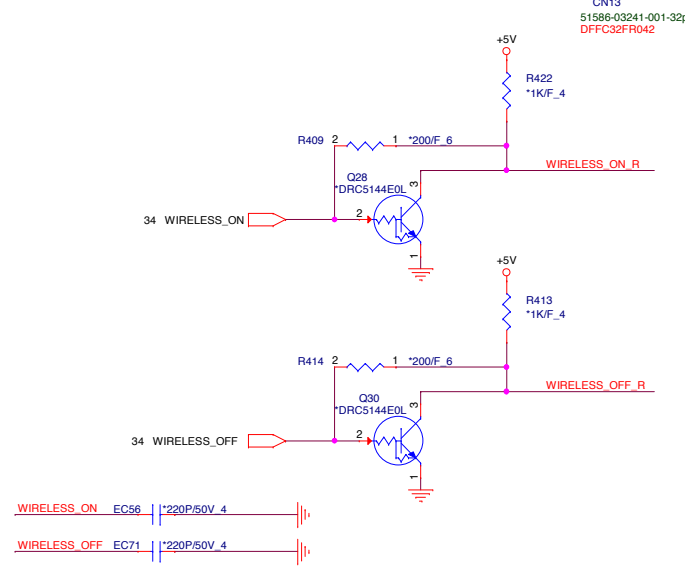
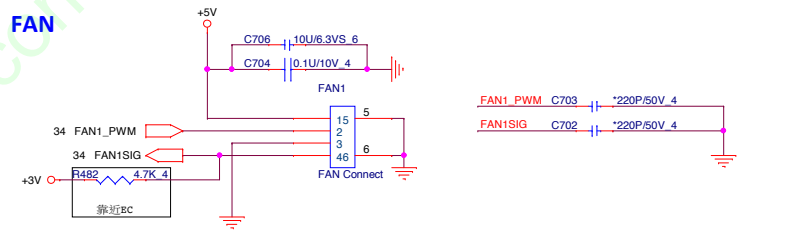
KEYBOARD Con.



KEYBOARD PULL-UP

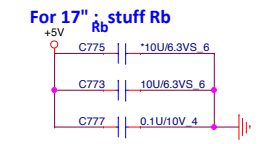
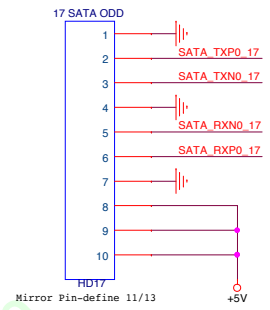
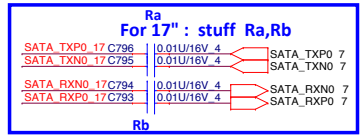


FAN



# HDD

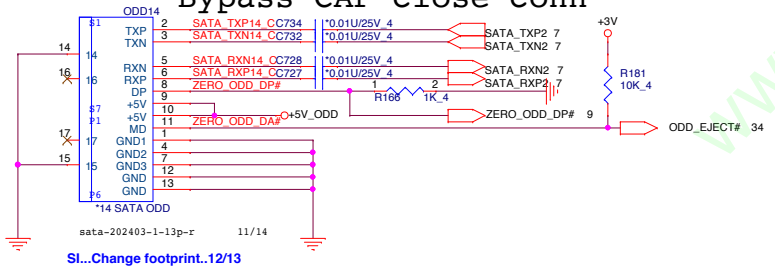
## SATA HDD Connector(Cable type) 15.6"



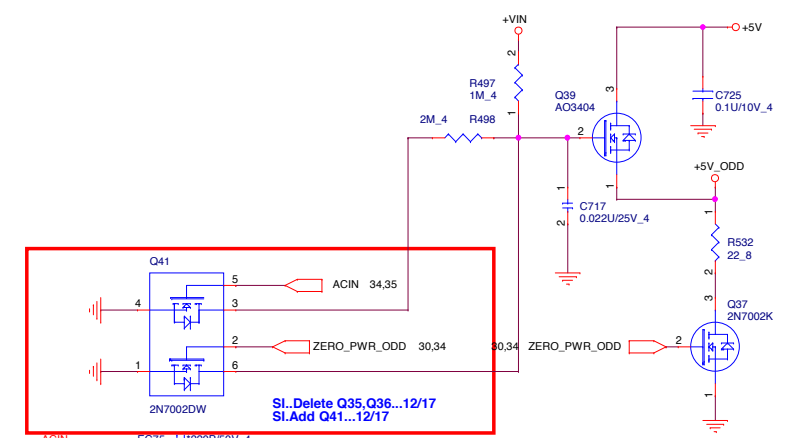
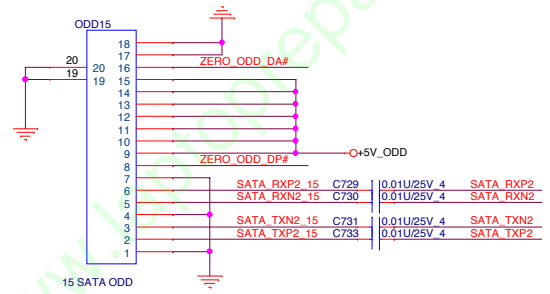
## SATA ODD CONNECTOR

### 14" SATA ODD

Bypass CAP close conn



### 15" SATA ODD



High : ODD power down  
Low : ODD power on

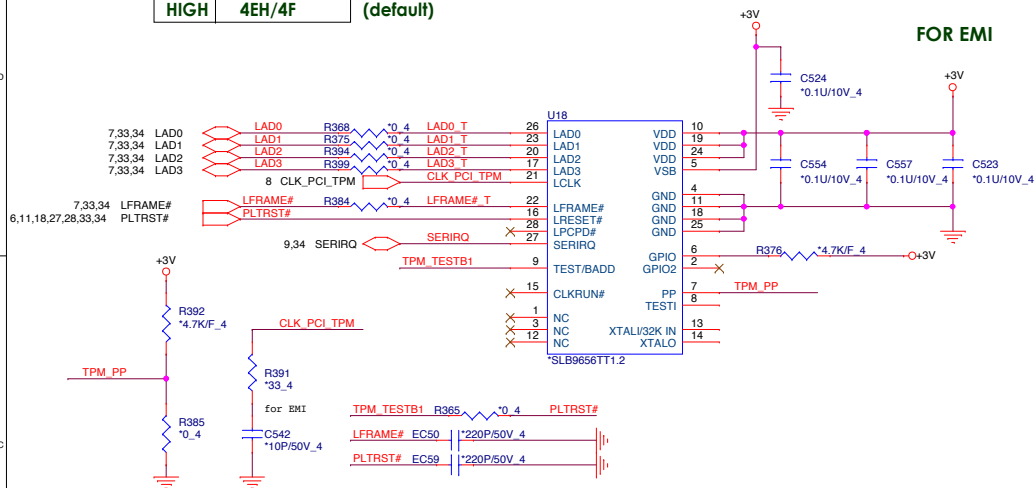
**PROJECT : Y11C**  
**Quanta Computer Inc.**

Size Custom	Document Number	Rev 1A
HDMI		
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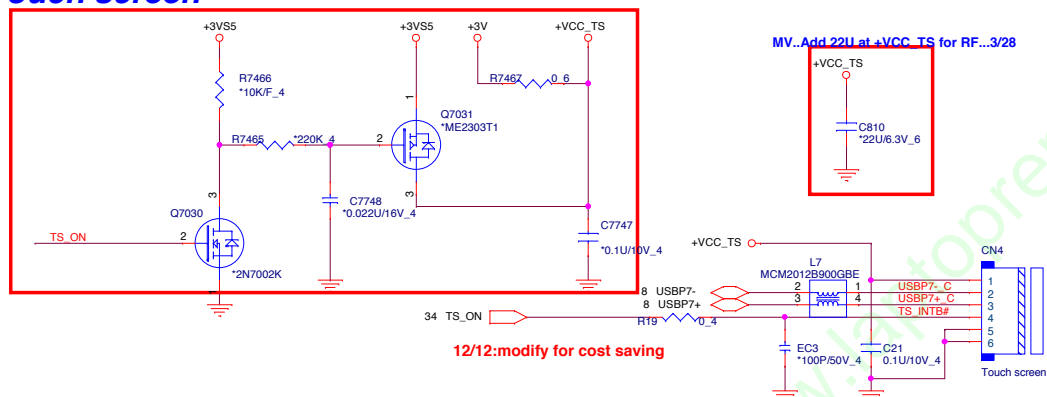
## TPM (1.2)

**Address**

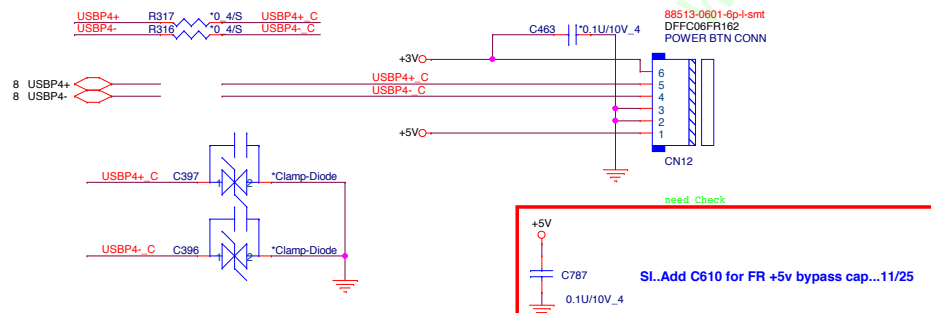
	<b>BADD</b>
<b>HIGH</b>	<b>4EH/4F</b> (default)



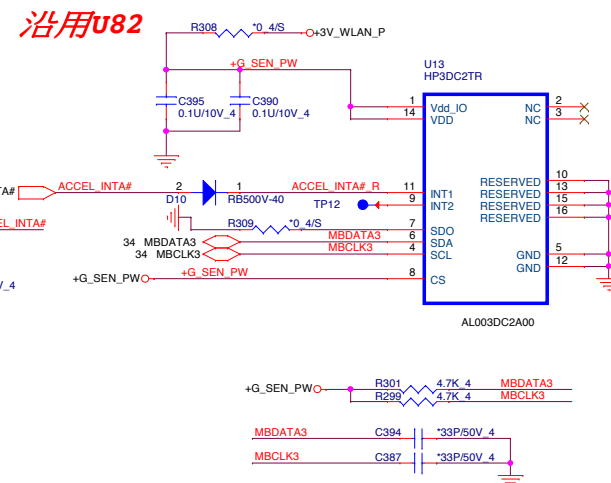
## Touch screen



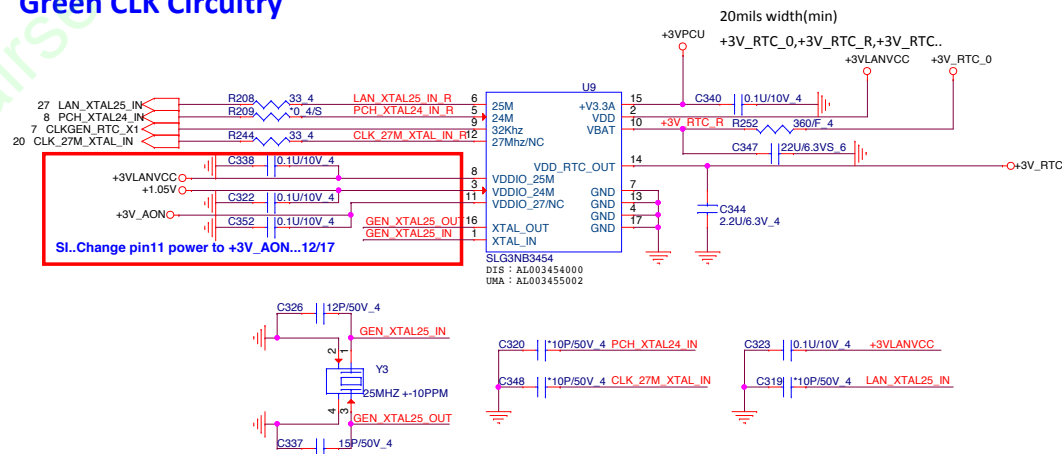
## Fingerprint Conn



## Accelerometer Sensor



## Green CLK Circuitry

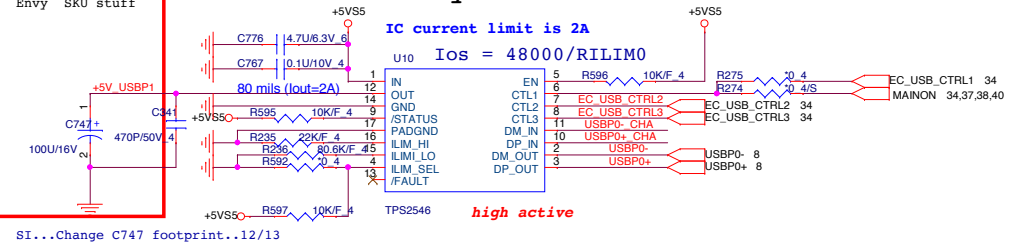


if Envy SKU,non-staff



For Pavilion / Envy SKU stuff

for Envy SKU stuff



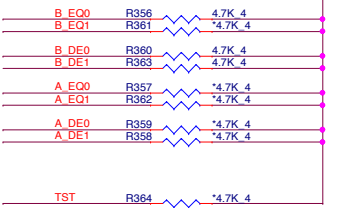
SI...Change C747 footprint..12/13

Right-Side USB3.0 Re-Driver

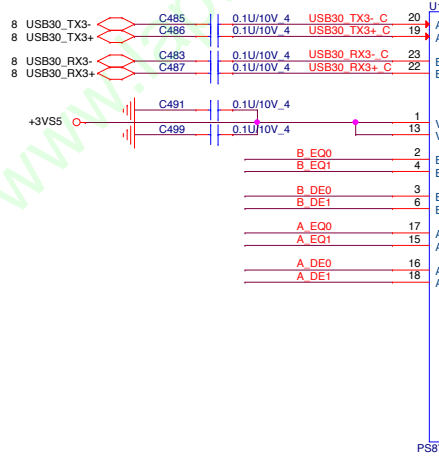
AEQ 9.5db / ADE 3.5db  
BEQ 13db / BDE 5db / REXT 5.36K

A_EQ1	A_EQ0	A_DE1	A_DE0	
B_EQ1	B_EQ0	B_DE1	B_DE0	
0	0	9.5dB	0	3.5dB
0	1	13dB	0	no de-emphasis
1	0	4.5dB	1	2.7dB
1	1	7.5dB	1	5dB

TST : Low = Normal LFPS swing / Right =Turn down LFPS swing

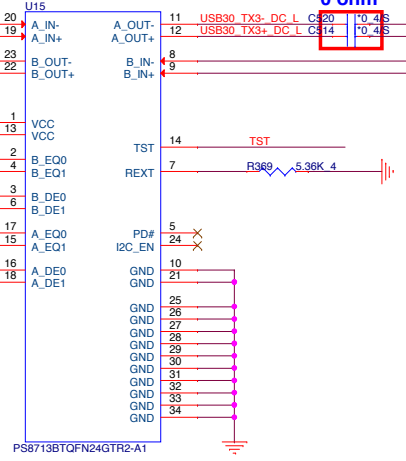


From HOST

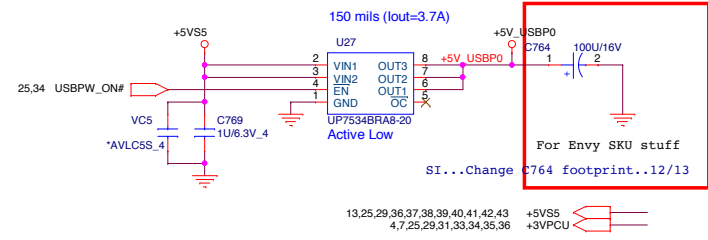
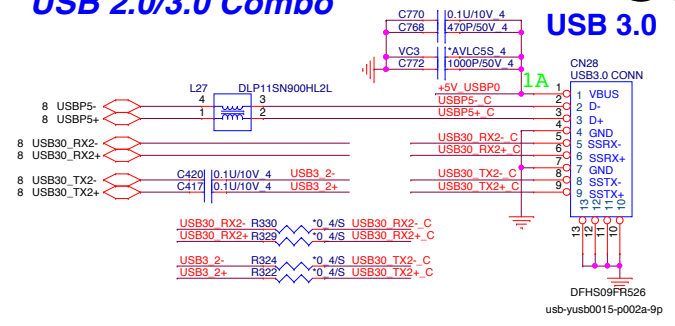


USB3.0 Re-driver

To Connector



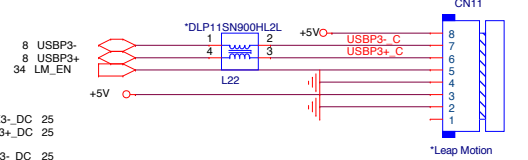
USB 2.0/3.0 Combo

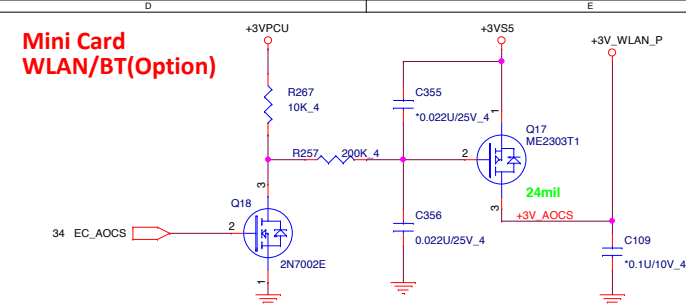
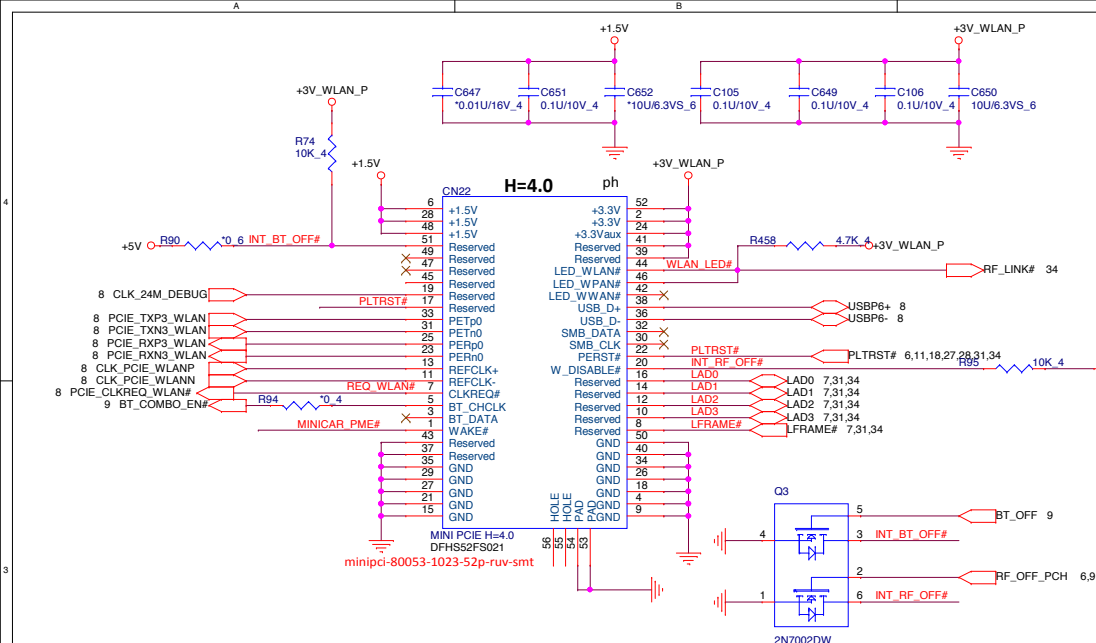


For Envy SKU stuff

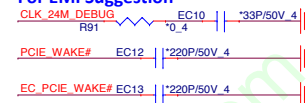
SI...Change C764 footprint..12/13

DFFC08FR016

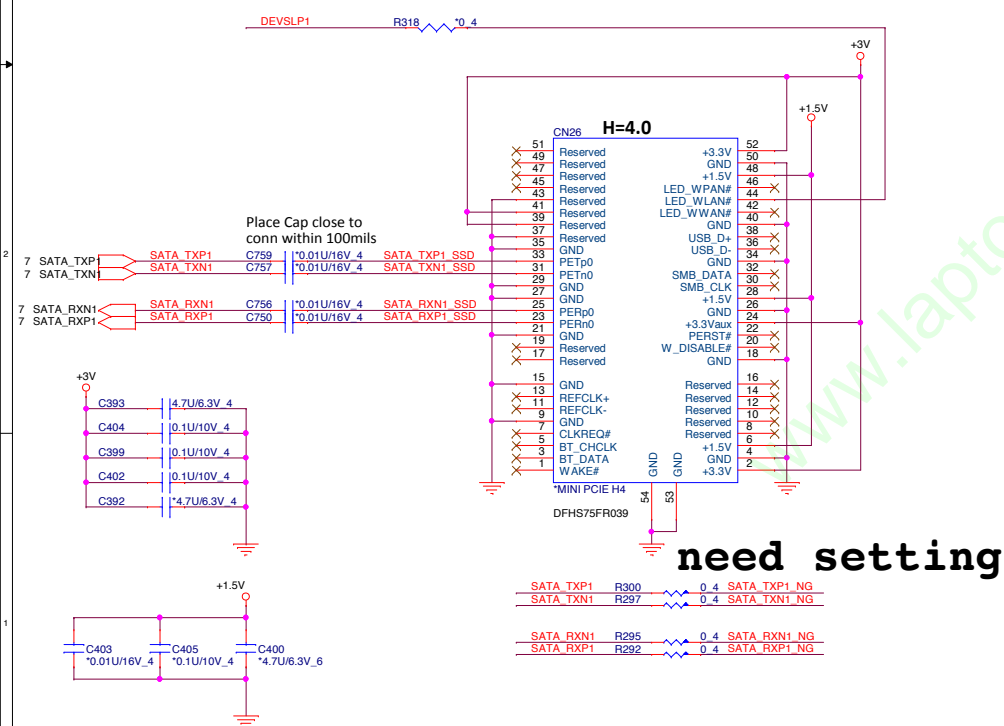
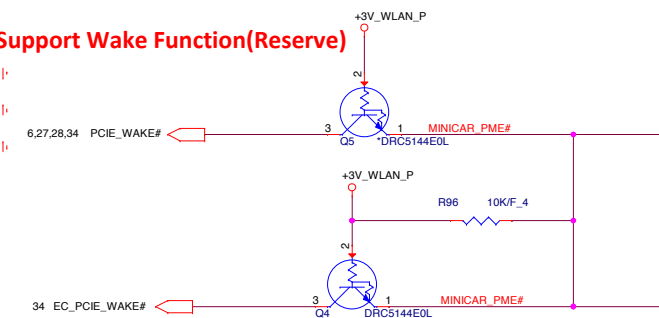




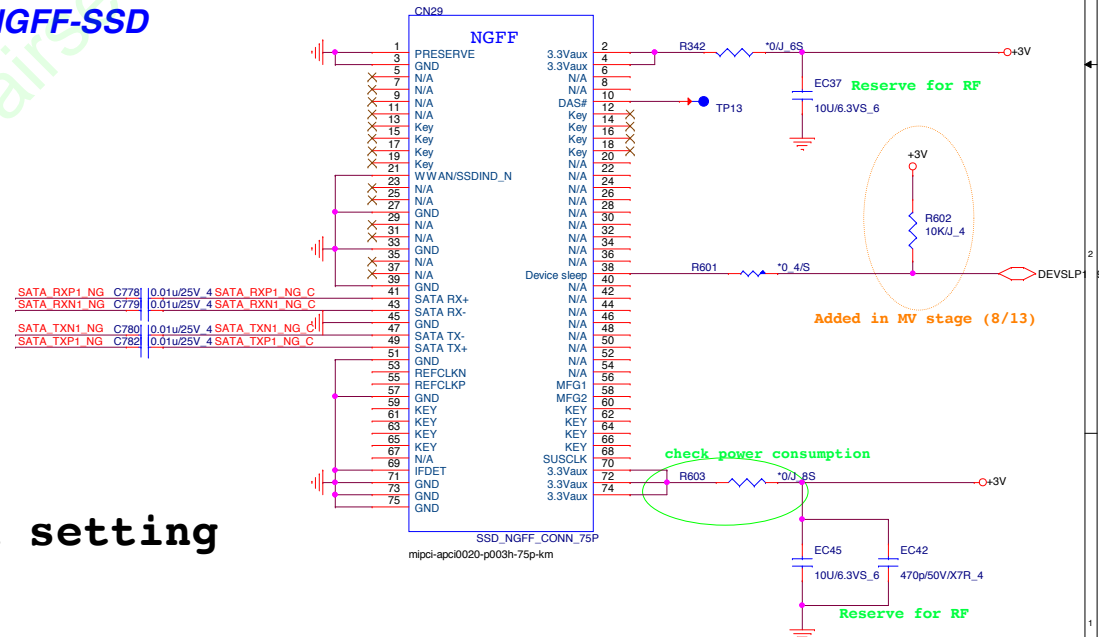
For EMI Suggestion



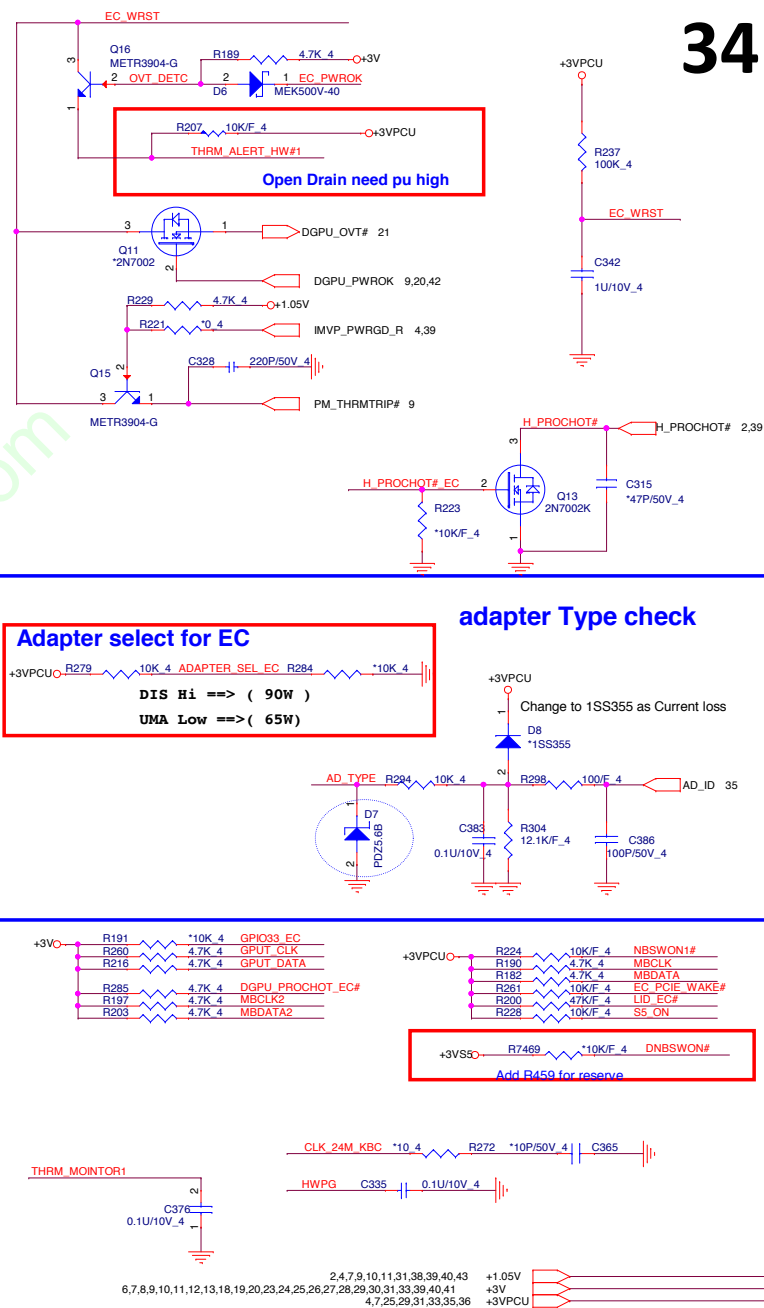
### Support Wake Function(Reserve)

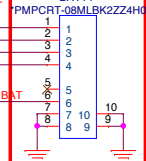


## NGFF-SSD

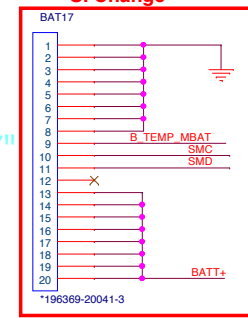


**need setting**



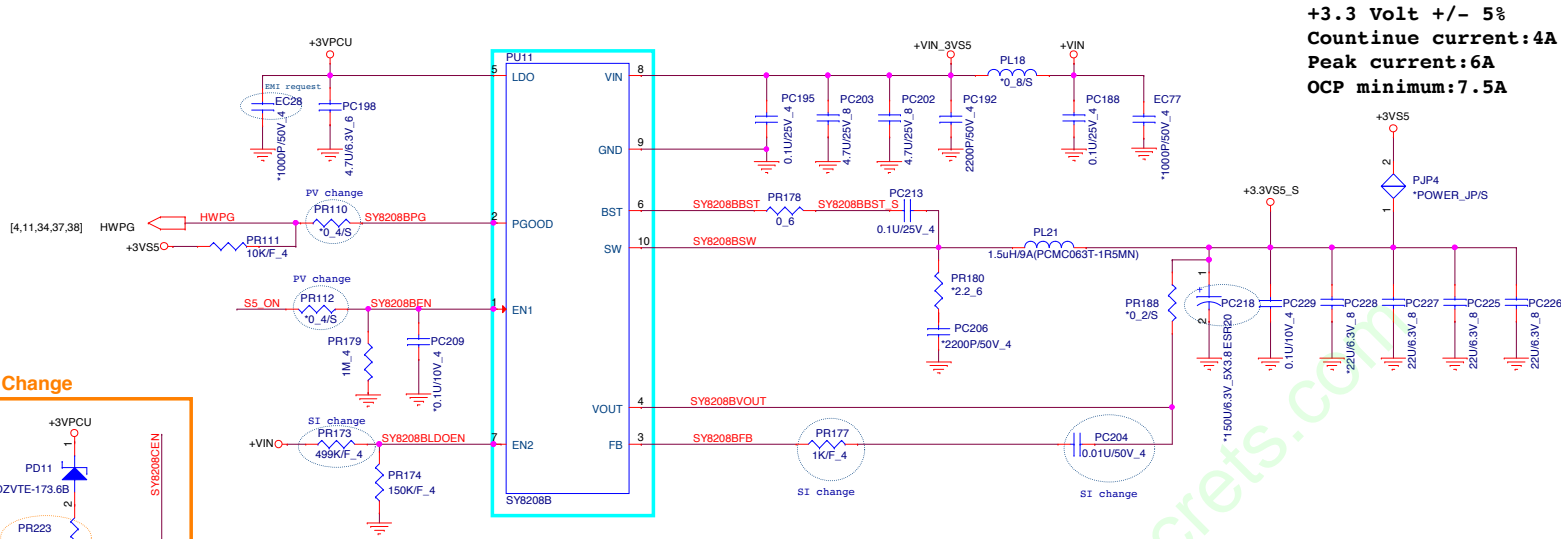
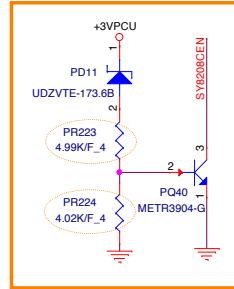


Place this cap  
close to EQ



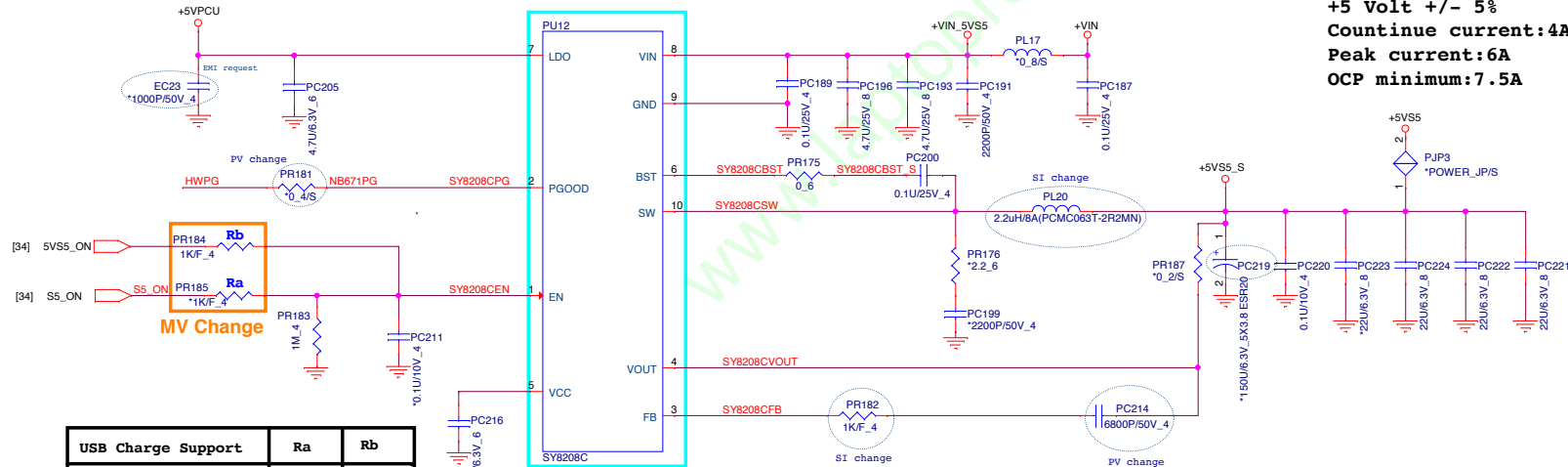
## DC/DC +3VS5/+5VS5

## MV Change

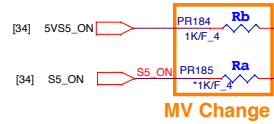


**+3.3 Volt +/- 5%**  
**Countinue current:4A**  
**Peak current:6A**  
**OCP minimum:7.5A**

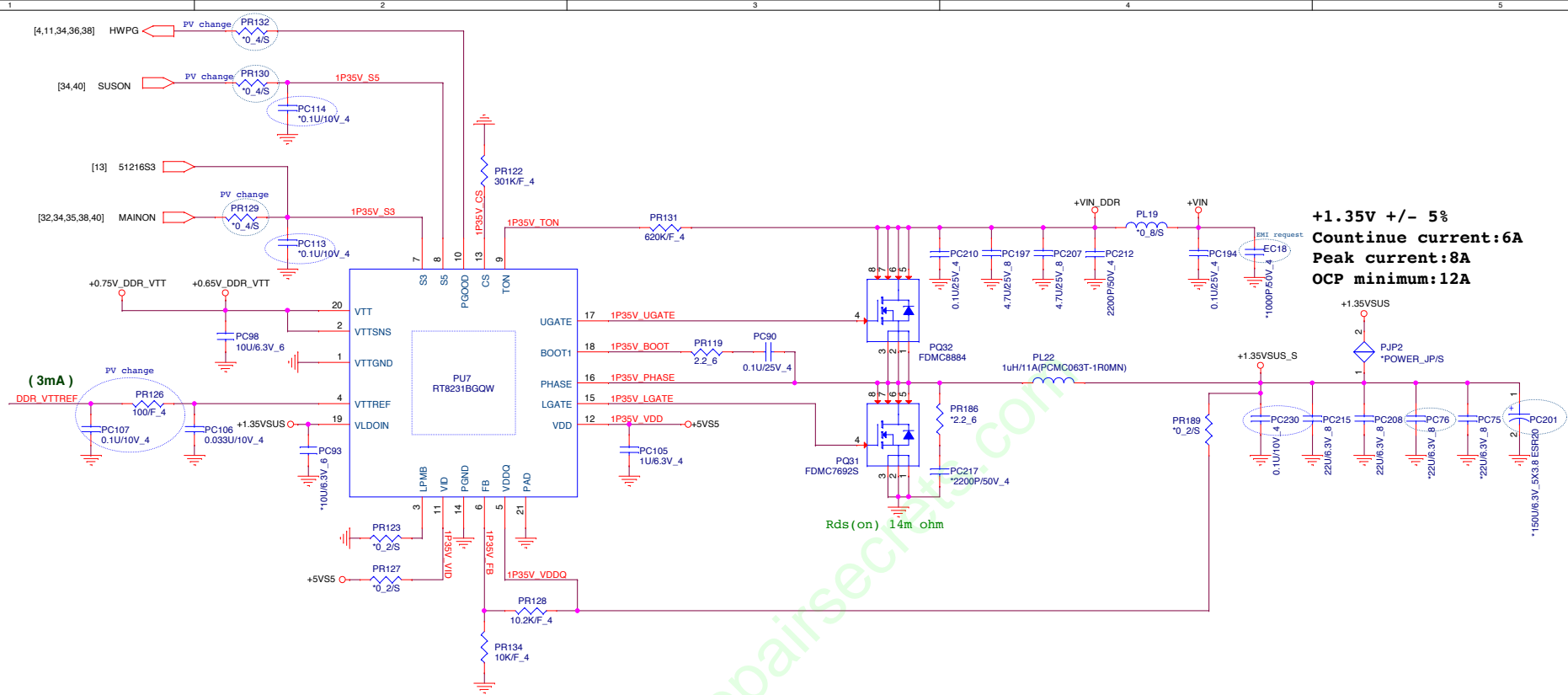
+3VS5 [6,7,9,10,11,25,29,32,33,38,40,43]  
+5VS5 [13,25,29,32,37,38,39,40,41,42,43]



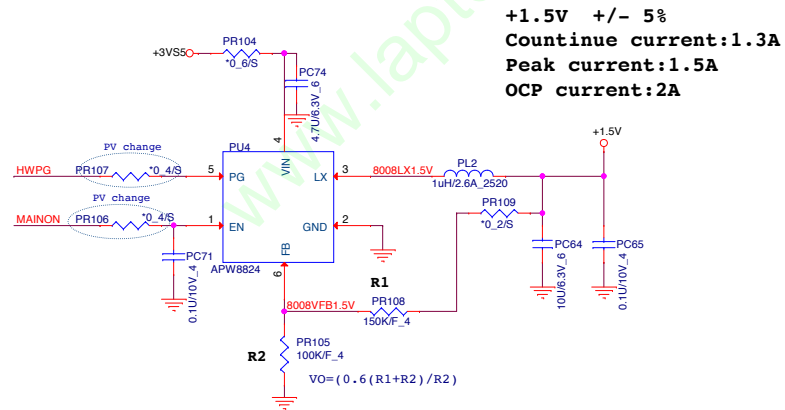
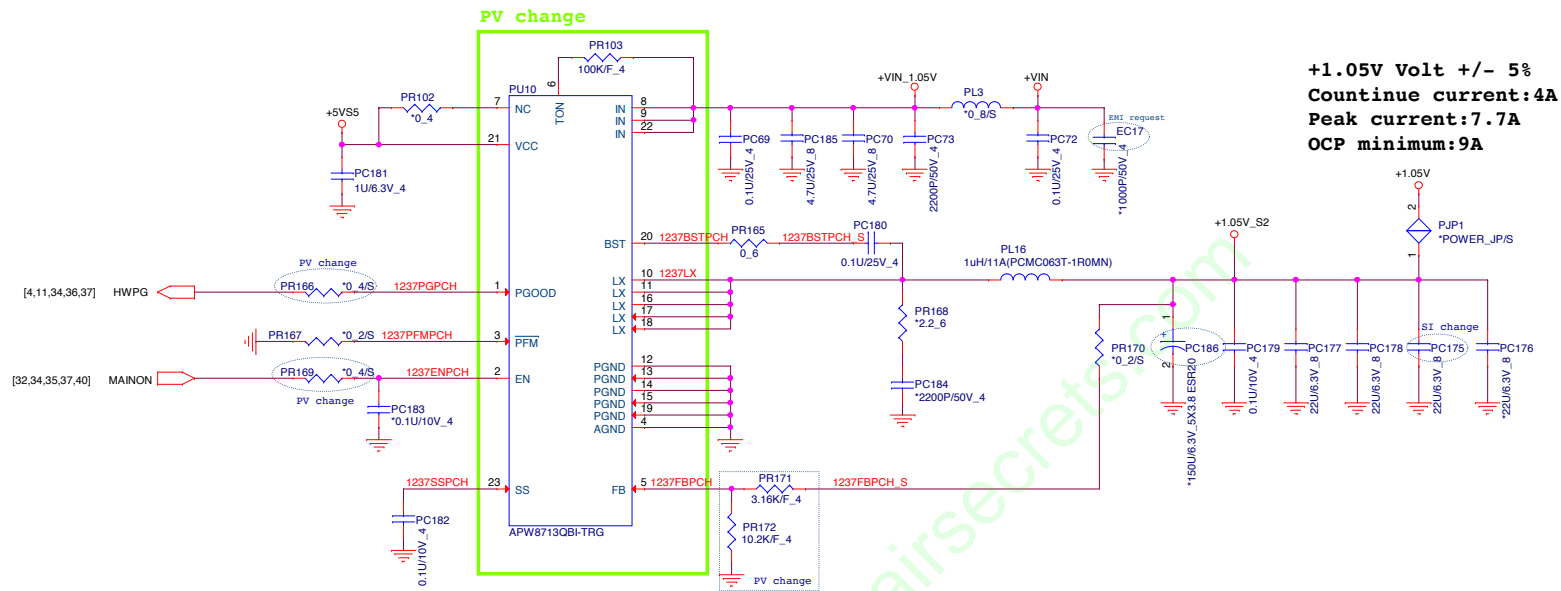
**+5 Volt +/- 5%**  
**Countinue current:4A**  
**Peak current:6A**  
**OCP minimum:7.5A**



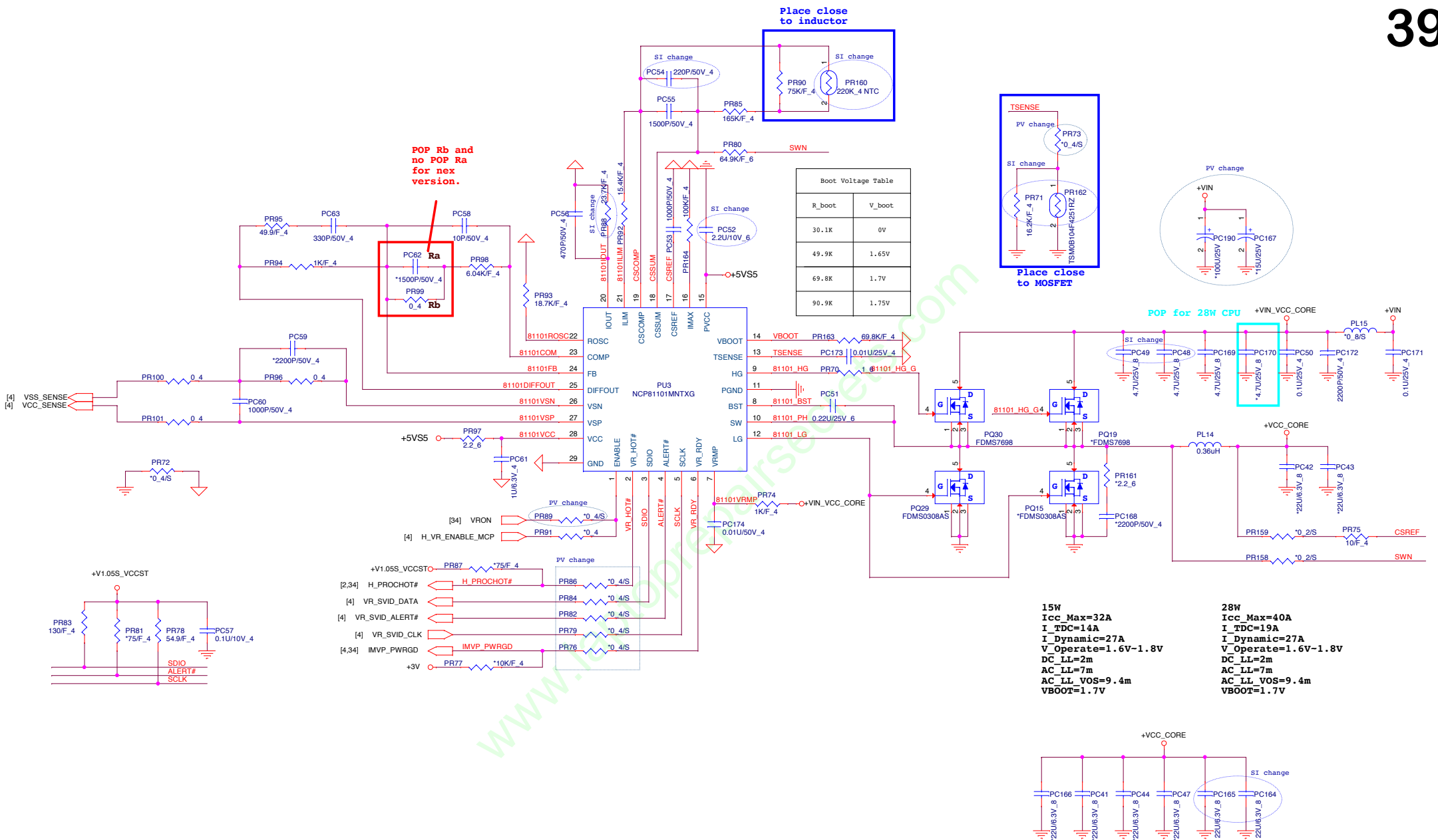
USB Charge Support	Ra	Rb
VINE (No support)	Stuff	NA
ENVY (Support)	NA	Stuff

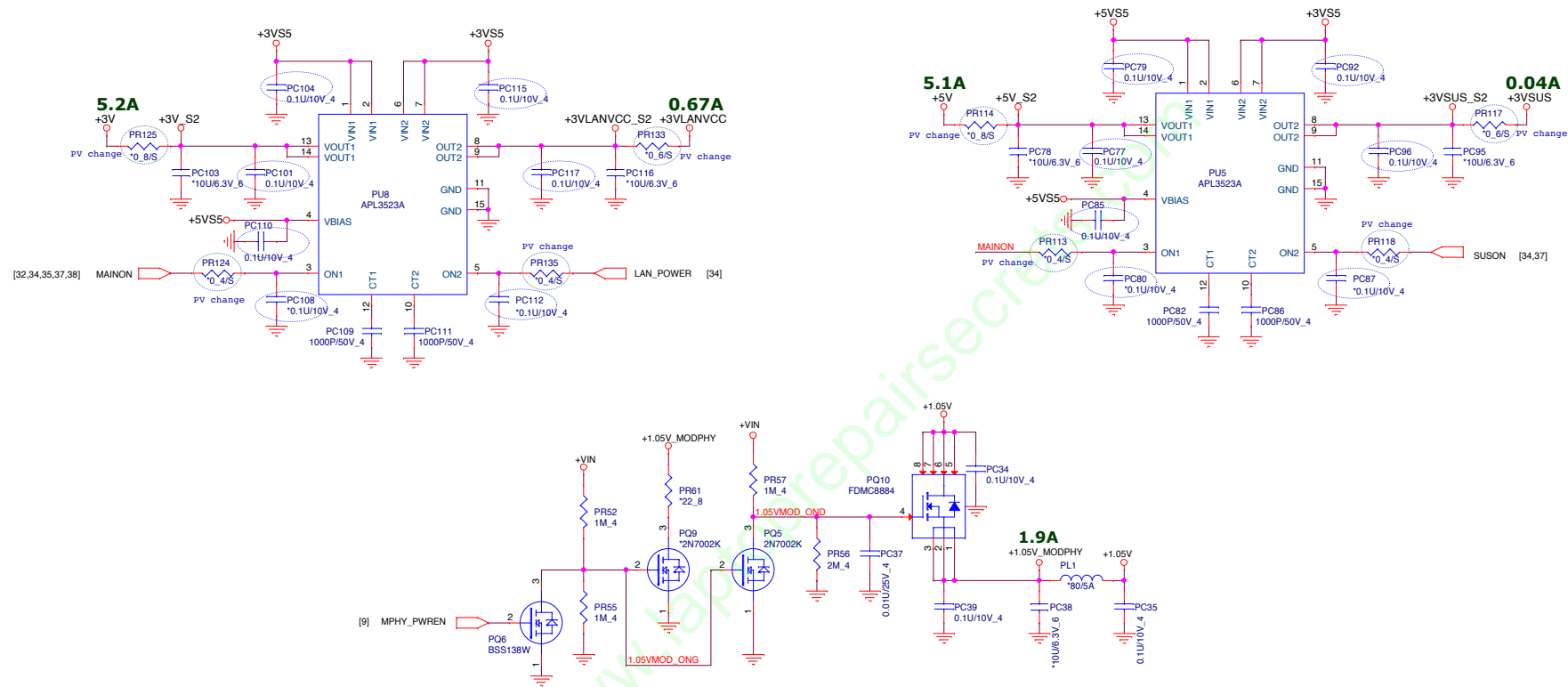


+1.35VSUS [2,4,12,13,29]



+VIN [24,26,29,30,35,36,37,39,40,41,42,43]  
+3VS5 [6,7,9,10,11,25,29,32,33,36,40,43]  
+5VS5 [13,25,29,32,36,37,39,40,41,42,43]





[6,7,8,9,10,11,12,13,14,17,18,23,24,25,26,27,28,29,30,31,33,34,39,41] +3V  
 [24,25,26,29,30,31,32,33] +5V  
 [24,26,29,30,35,36,37,38,39,41,42,43] +VIN  
 [6,7,9,10,11,25,29,32,33,36,38,43] +3VS5  
 [13,25,29,32,36,37,38,39,41,42,43] +3VS5  
 [27,31] +3VLANVCC



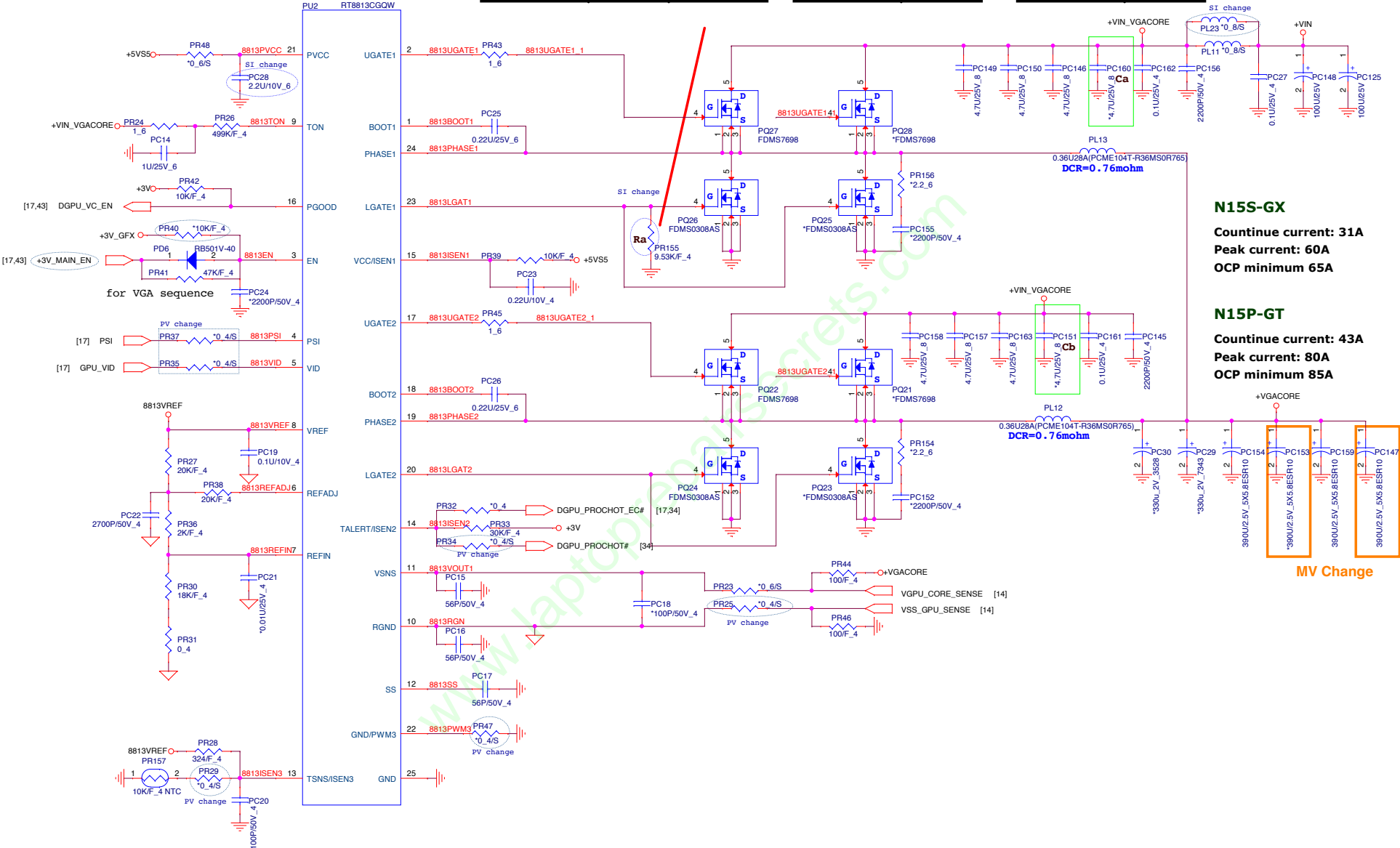
**PROJECT :U83**  
**Quanta Computer Inc.**

Size Custom	Document Number <b>Dis-charge IC (SLG55448)</b>	Rev 1A
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VGA TYPE	Ra Value	P/N
N15S-GX (25W)	9.53K	CS29532FB10
N15P-GT (35W)	12.4K	CS31242FB13

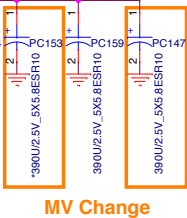
VGA TYPE	MOSFET
N15S-GX (25W)	1H1L
N15P-GT (35W)	2H2L

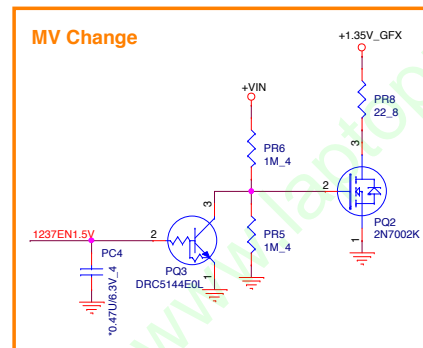
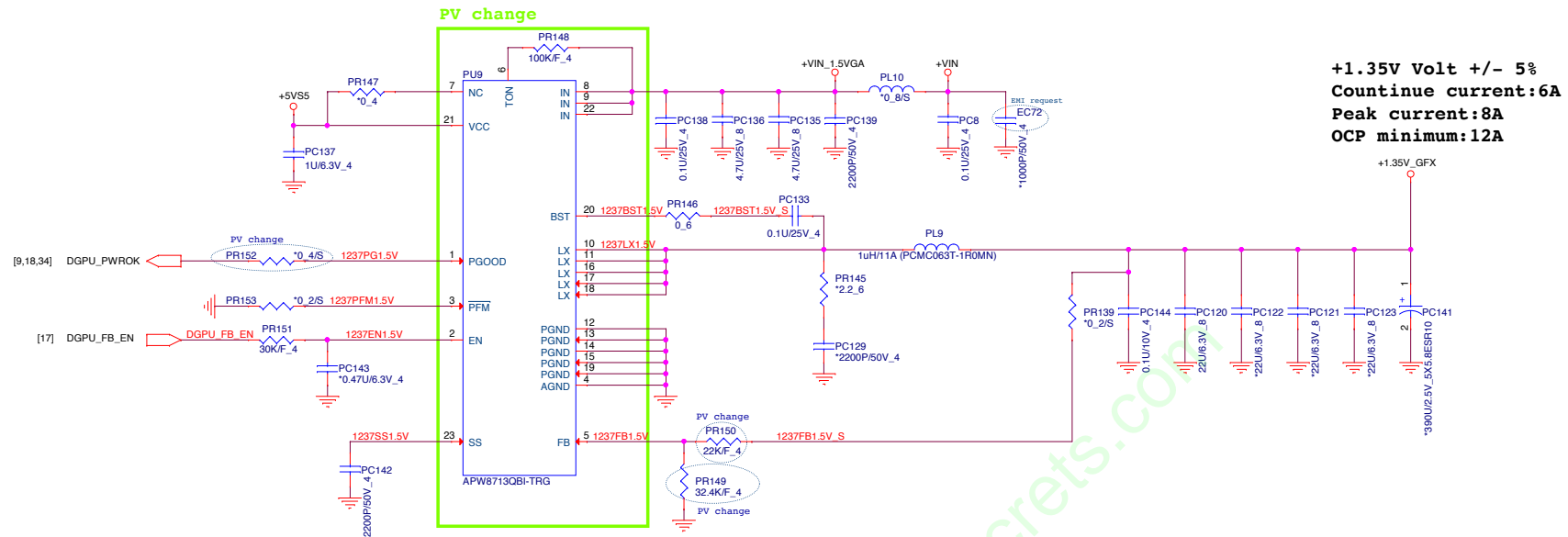
VGA TYPE	Ca, Cb
N15S-GX (25W)	No stuff
N15P-GT (35W)	Stuff

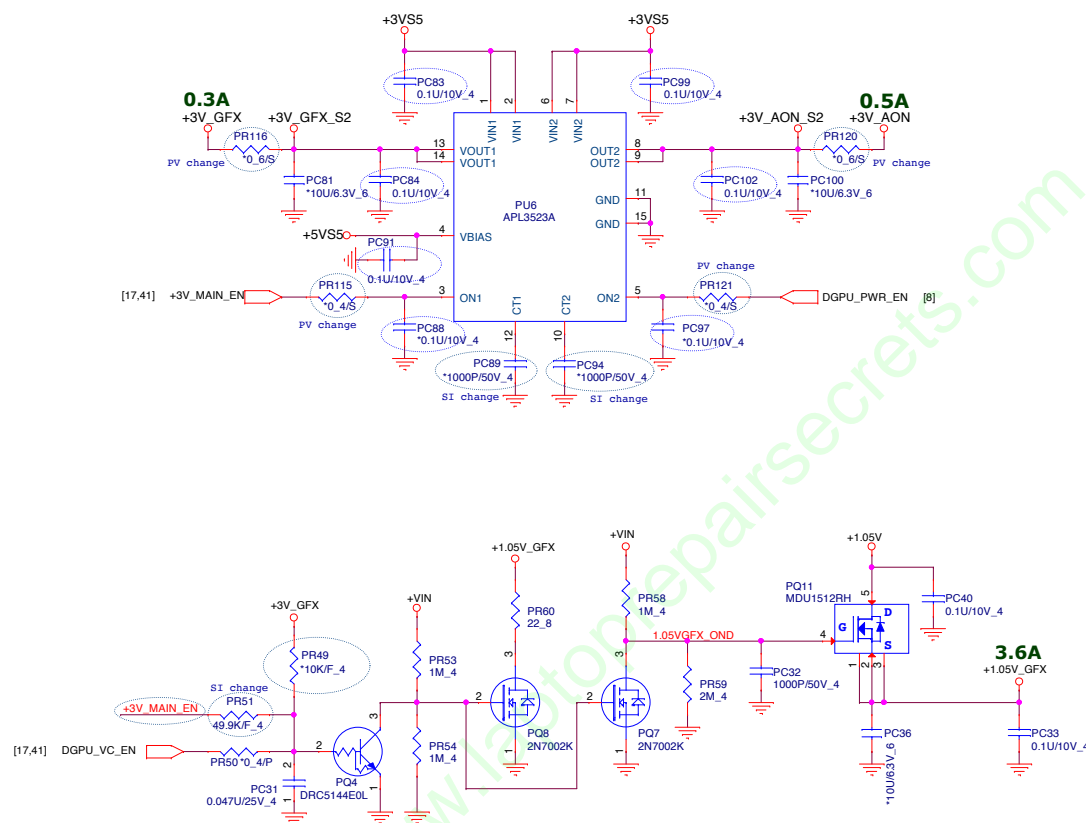


**N15S-GX**  
Countinue current: 31A  
Peak current: 60A  
OCP minimum 65A

**N15P-GT**  
Countinue current: 43A  
Peak current: 80A  
OCP minimum 85A







Battery Connector	Pavillion	ENVY
14"	DFAD08MR063	DFAD08MR064
15"	DFAD08MR065	DFAD08MR066
17"	DFFC20FR081	DFFC20FR081

USB Charge Support	PR185	PR184
Pavillion	Stuff	NA
ENVY (USB charge)	NA	Stuff

UMA	Disable Page 41 、 42 、 43 ,but keep below location
Page 41	PC161 、 PC162
Page 42	PC138 、 PC144 、 PC4 、 PC148
Page 43	PC84 、 PC102 、 PC88 、 PC97 、 PC40 、 PC33

Discrete	Location	Part Number
N15S (25W)	PR155	CS29532FB10
	PC151 、 PC160	NA
	PQ21 、 PQ23 、 PQ25 、 PQ28	NA
N15P (35W)	PR155	CS31242FB13
	PC151 、 PC160	Stuff
	PQ21 、 PQ23 、 PQ25 、 PQ28	Stuff

Title <Title>		
Size A	Document Number <Doc>	Rev <RevCode>
Date:	Wednesday, April 02, 2014	Sheet 44 of 44

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